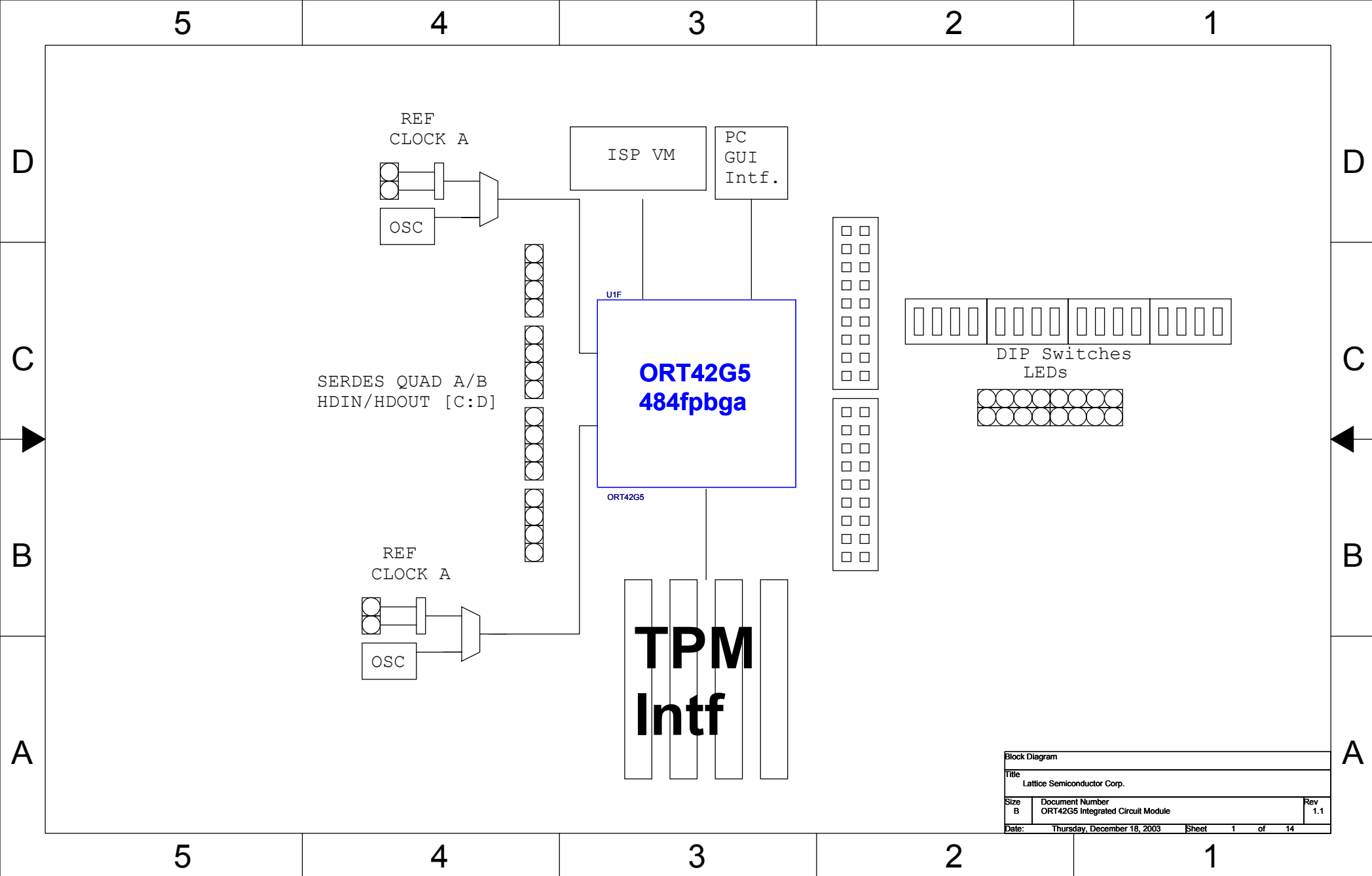
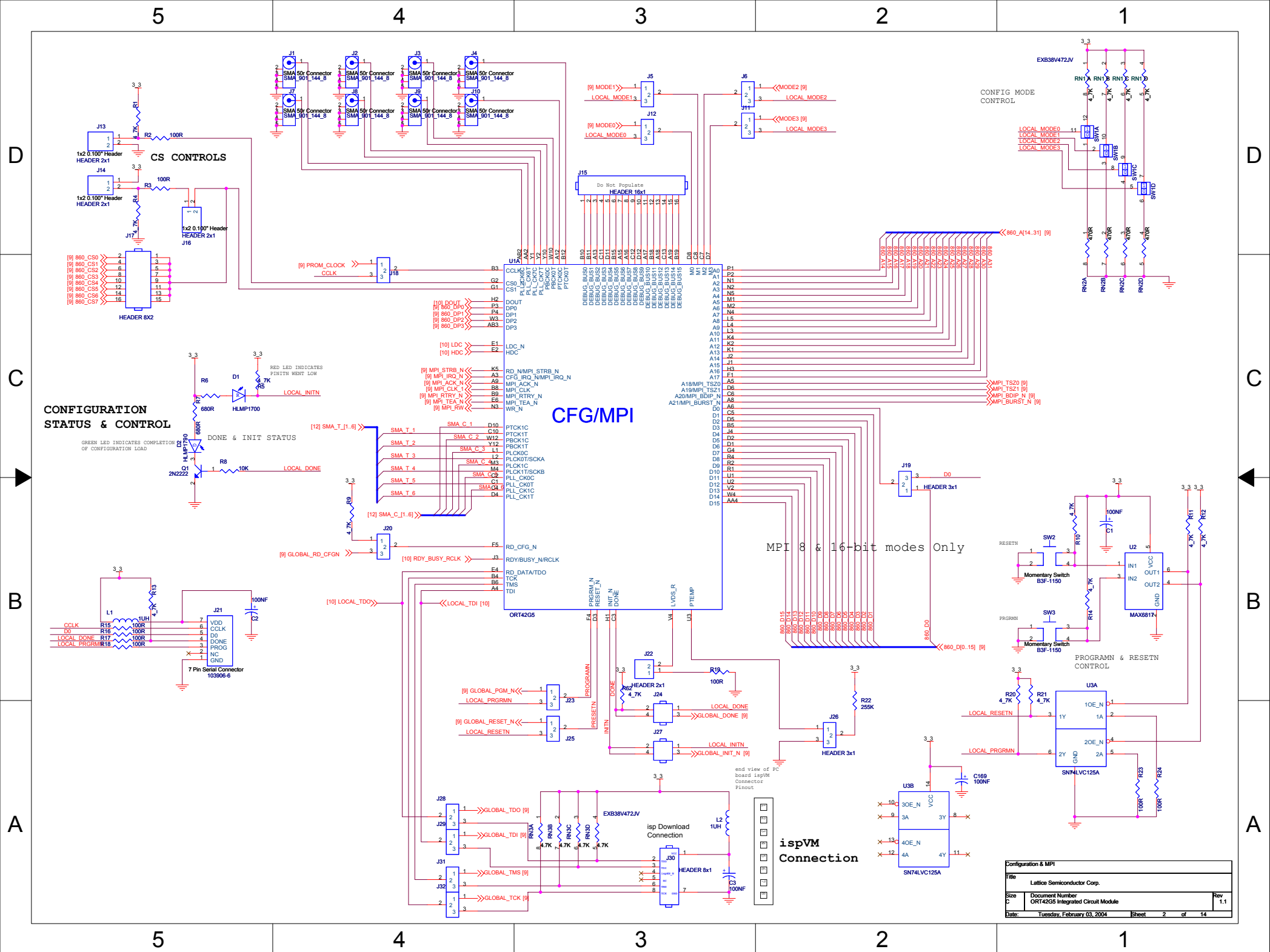






D

C

B

A

U1B
ORT42G5

General
FPGA
Pins

[8,10]	GEN_FPGA_PIN0	GPIO	A0
[8,10]	GEN_FPGA_PIN1	GPIO	A1
[8,10]	GEN_FPGA_PIN2	GPIO	A2
[8,10]	GEN_FPGA_PIN3	GPIO	A3
[8,10]	GEN_FPGA_PIN4	GPIO	A4
[8,10]	GEN_FPGA_PIN5	GPIO	A5
[8,10]	GEN_FPGA_PIN6	GPIO	A6
[8,10]	GEN_FPGA_PIN7	GPIO	A7
[8,10]	GEN_FPGA_PIN8	GPIO	A8
[8,10]	GEN_FPGA_PIN9	GPIO	A9
[8,10]	GEN_FPGA_PIN10	GPIO	A10
[8,10]	GEN_FPGA_PIN11	GPIO	A11
[8,10]	GEN_FPGA_PIN12	GPIO	A12
[8,10]	GEN_FPGA_PIN13	GPIO	A13
[8,10]	GEN_FPGA_PIN14	GPIO	A14
[8,10]	GEN_FPGA_PIN15	GPIO	A15
[8,10]	GEN_FPGA_PIN16	GPIO	A0
[8,10]	GEN_FPGA_PIN17	GPIO	A1
[8,10]	GEN_FPGA_PIN18	GPIO	A2
[8,10]	GEN_FPGA_PIN19	GPIO	A3
[8,10]	GEN_FPGA_PIN20	GPIO	A4
[8,10]	GEN_FPGA_PIN21	GPIO	A5
[8,10]	GEN_FPGA_PIN22	GPIO	A6
[8,10]	GEN_FPGA_PIN23	GPIO	A7
[8,10]	GEN_FPGA_PIN24	GPIO	A8
[8,10]	GEN_FPGA_PIN25	GPIO	A9
[8,10]	GEN_FPGA_PIN26	GPIO	A10
[8,10]	GEN_FPGA_PIN27	GPIO	A11
[8,10]	GEN_FPGA_PIN28	GPIO	A12
[8,10]	GEN_FPGA_PIN29	GPIO	A13
[8,10]	GEN_FPGA_PIN30	GPIO	A14
[8,10]	GEN_FPGA_PIN32	GPIO	A15
[8,10]	GEN_FPGA_PIN31	GPIO	A0
[8,10]	GEN_FPGA_PIN33	GPIO	A1
[8,10]	GEN_FPGA_PIN34	GPIO	A2
[8,10]	GEN_FPGA_PIN35	GPIO	A3
[8,10]	GEN_FPGA_PIN36	GPIO	A4
[8,10]	GEN_FPGA_PIN37	GPIO	A5
[8,10]	GEN_FPGA_PIN38	GPIO	A6
[8,10]	GEN_FPGA_PIN39	GPIO	A7
[8,10]	GEN_FPGA_PIN40	GPIO	A8
[8,10]	GEN_FPGA_PIN41	GPIO	A9
[8,10]	GEN_FPGA_PIN42	GPIO	A10
[8,10]	GEN_FPGA_PIN43	GPIO	A11
[8,10]	GEN_FPGA_PIN44	GPIO	A12
[8,10]	GEN_FPGA_PIN45	GPIO	A13
[8,10]	GEN_FPGA_PIN46	GPIO	A14
[8,10]	GEN_FPGA_PIN47	GPIO	A15

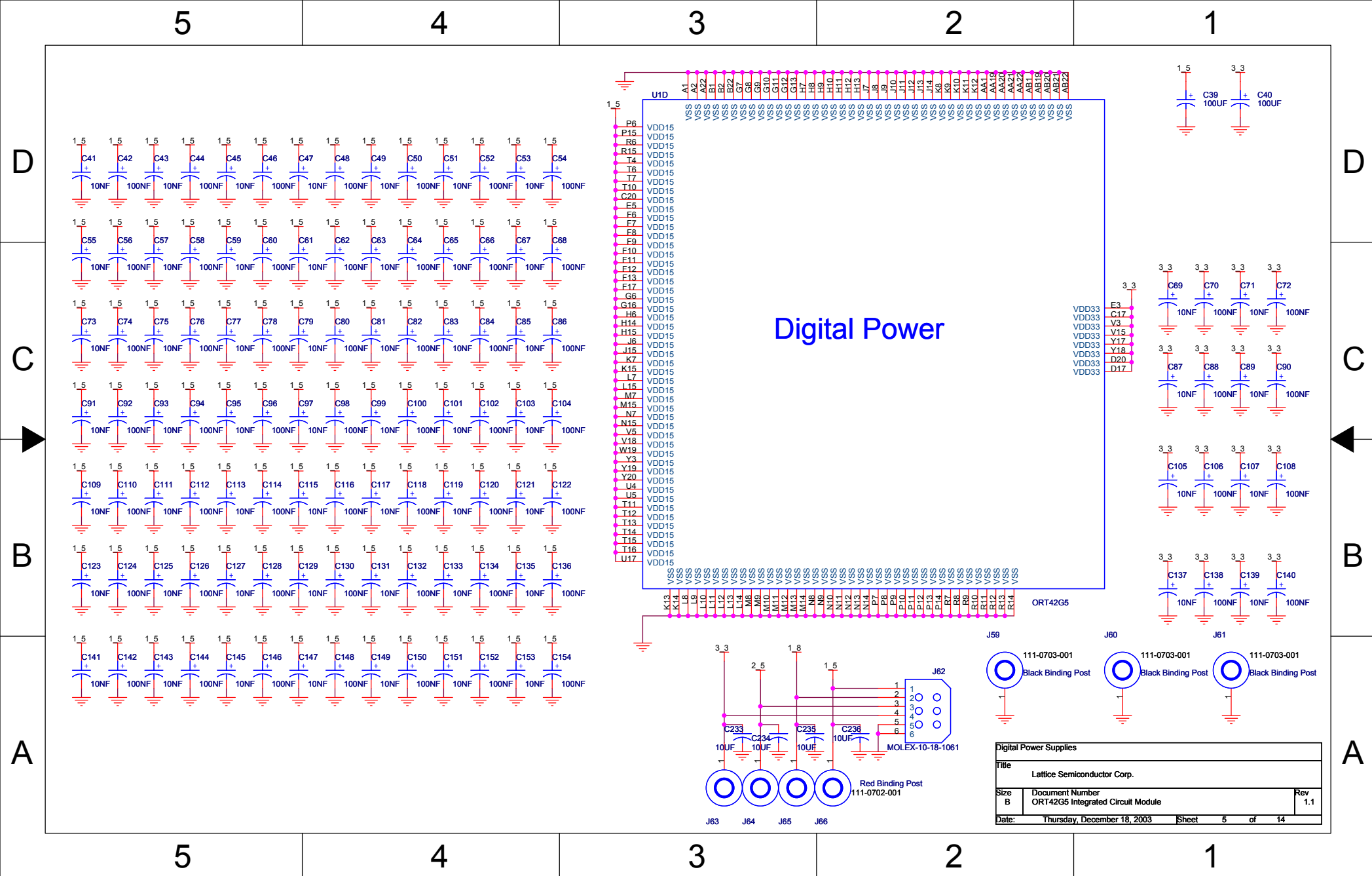
FPGA Pins

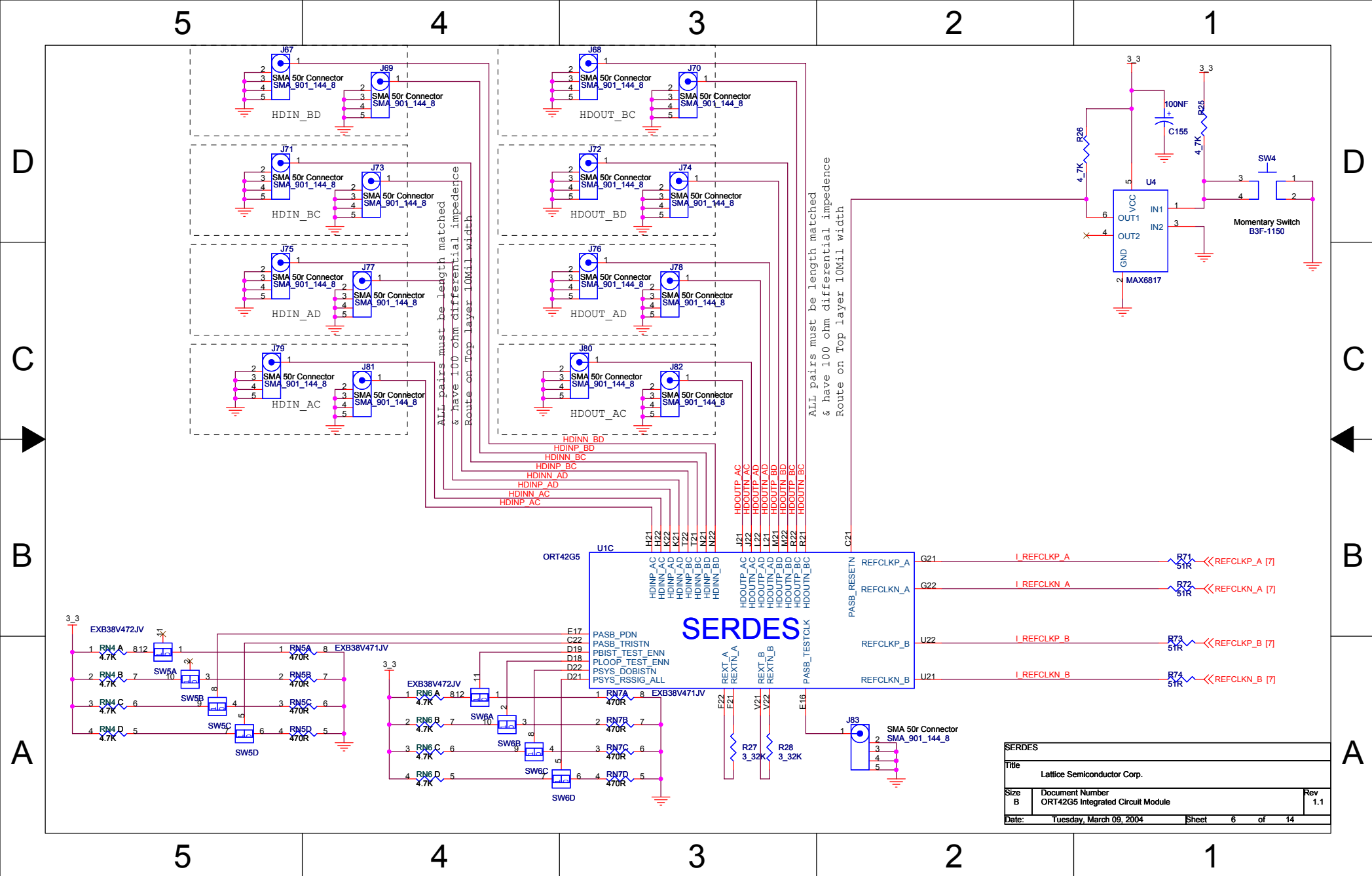
Title	Lattice Semiconductor Corp.
-------	-----------------------------

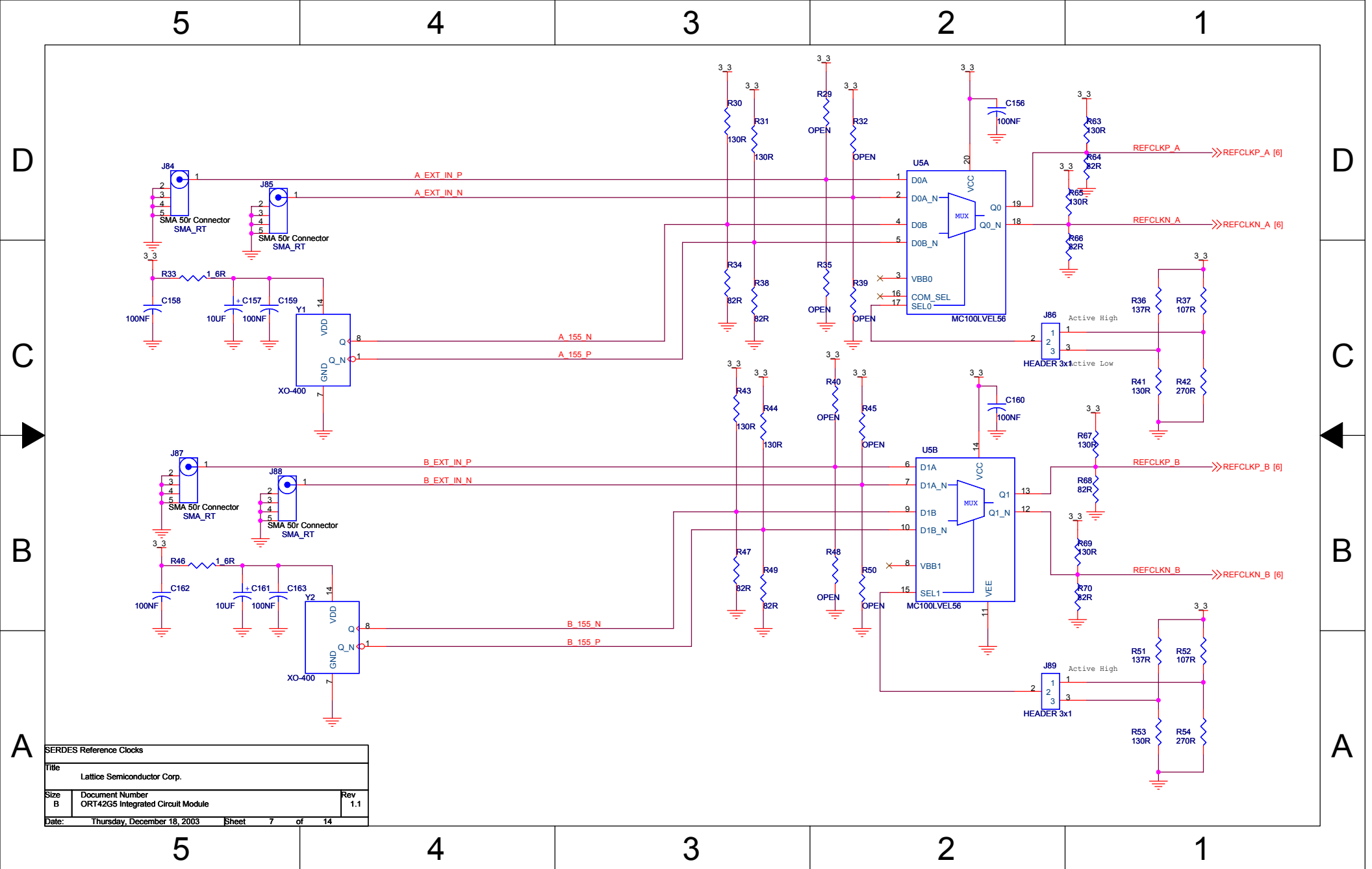
Size B	Document Number ORT42G5 Integrated Circuit Module
-----------	--

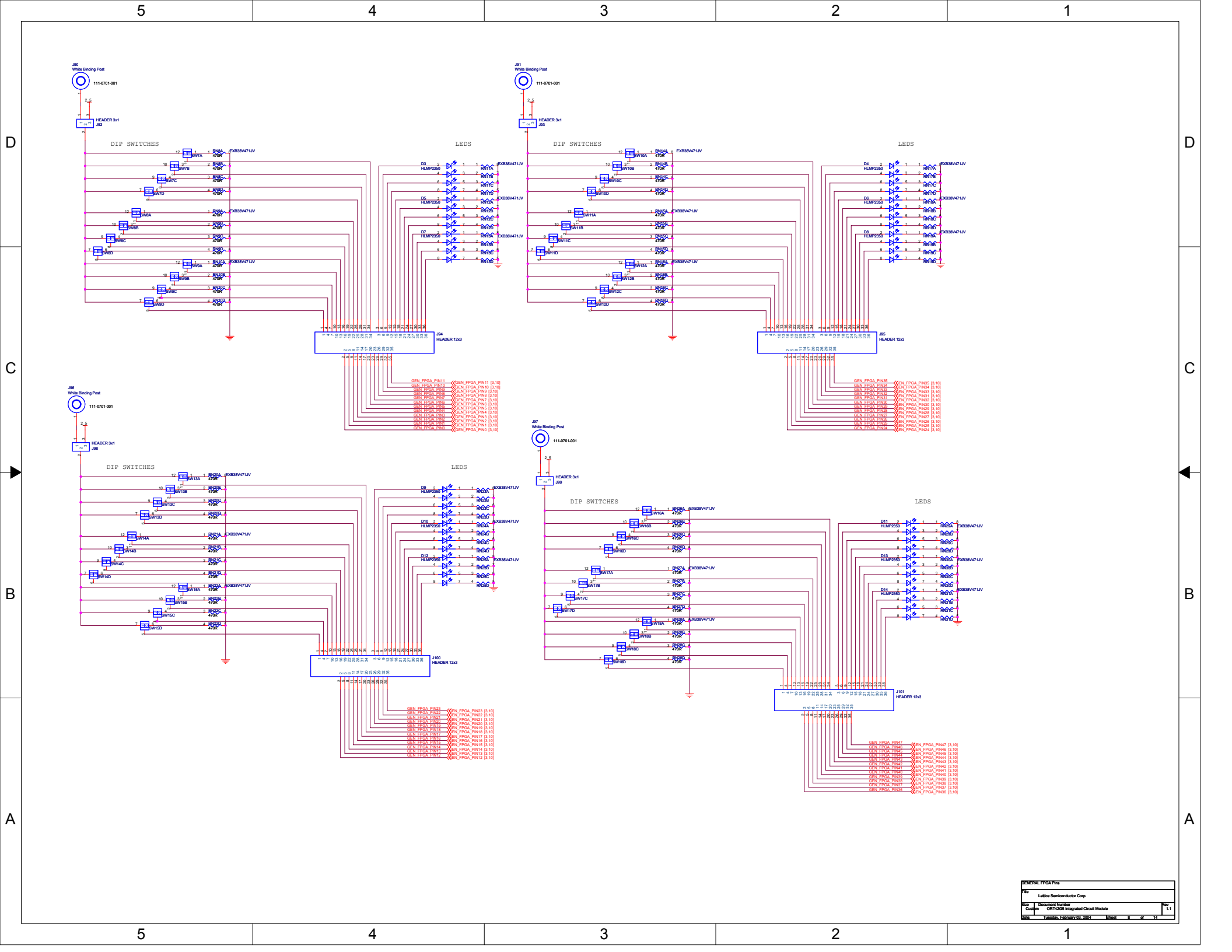
1.1

Date: Thursday, December 18, 2003 Sheet 3 of 14

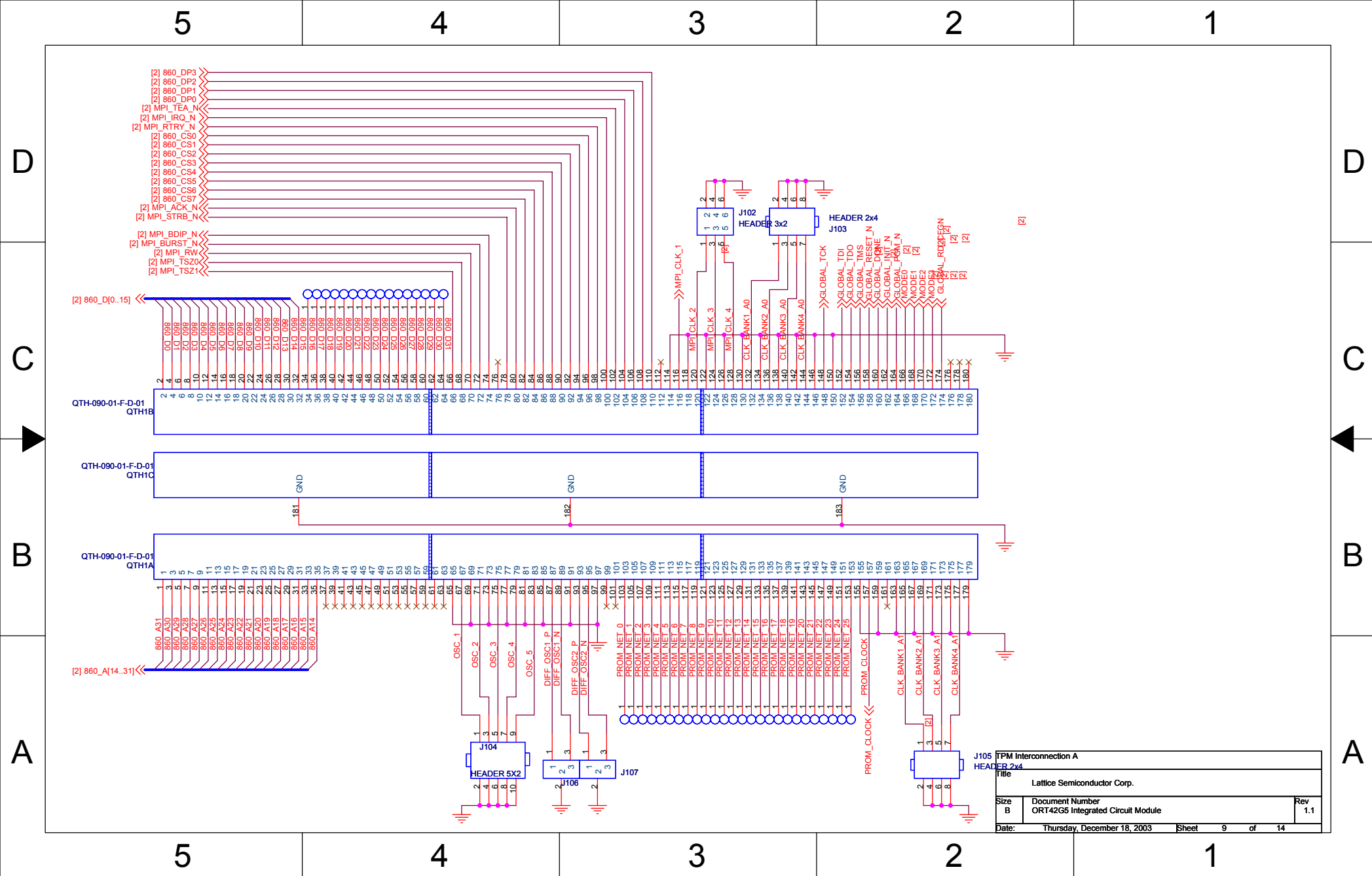


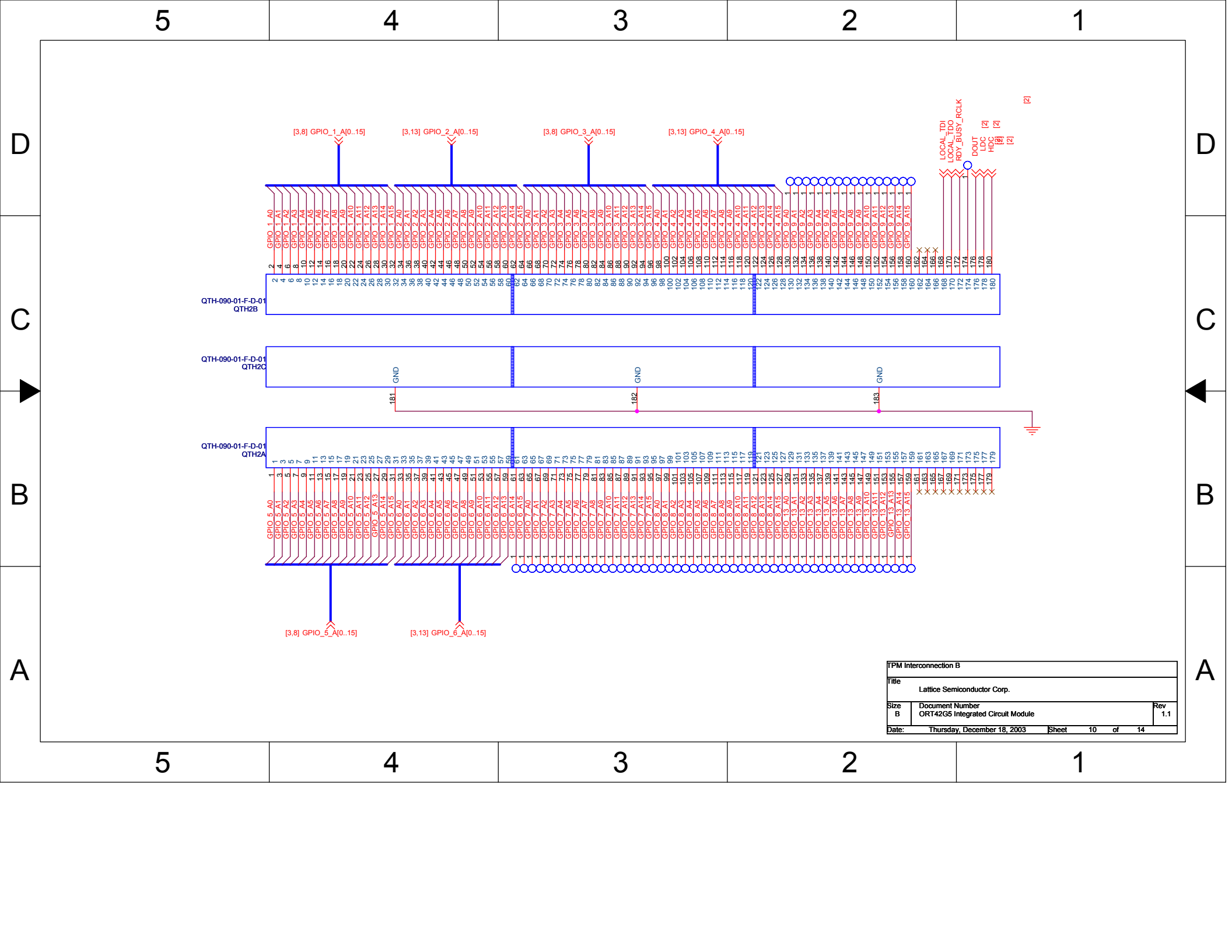




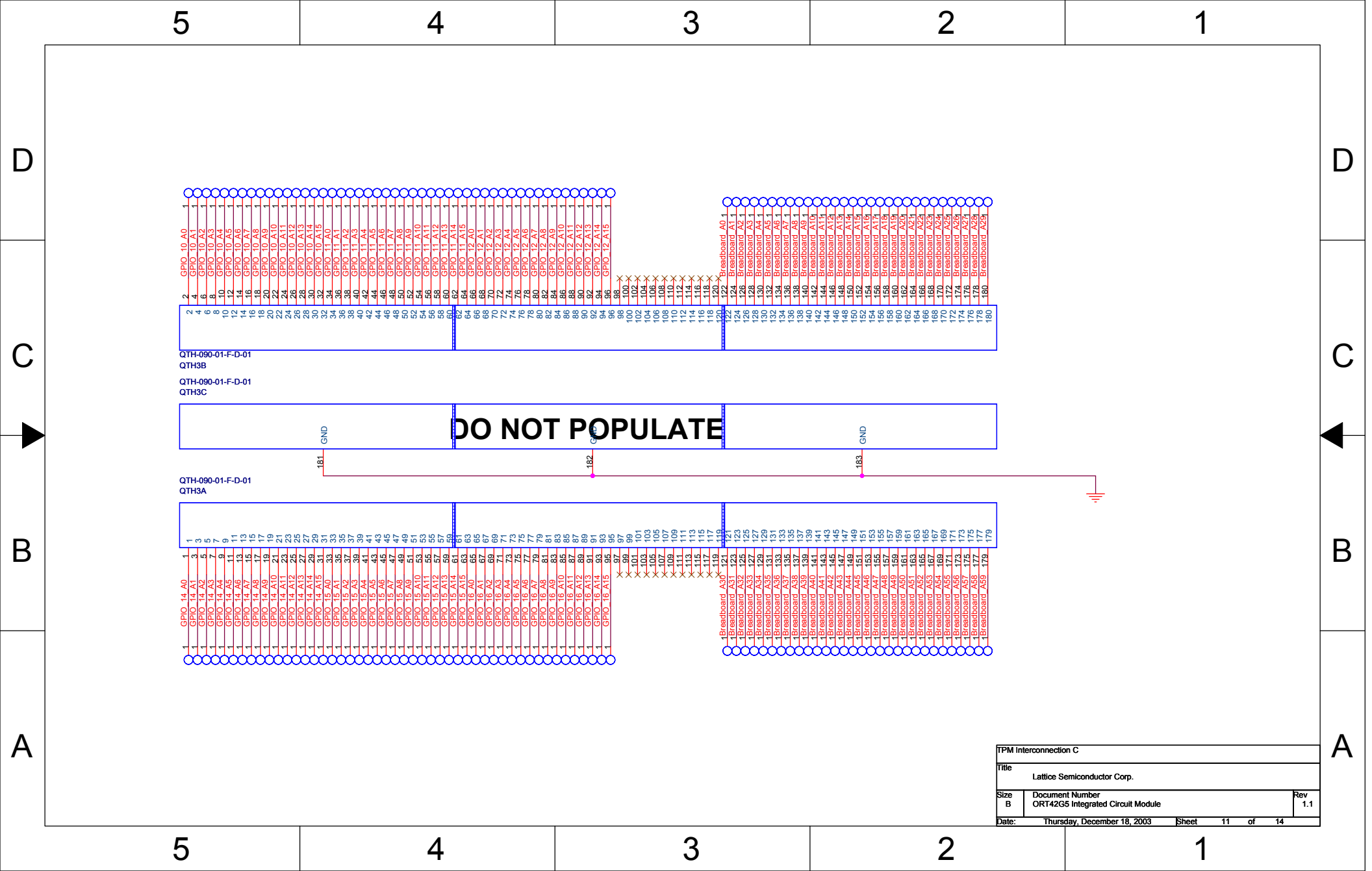


GENERAL FPGA PINS			
Pin	Lattice Semiconductor Corp.		
Doc	Document Number	Pin	
Class	ORT4205 Integrated Circuit Module	T1.1	
Rev	Version 1.0	Sheet	8 of 14

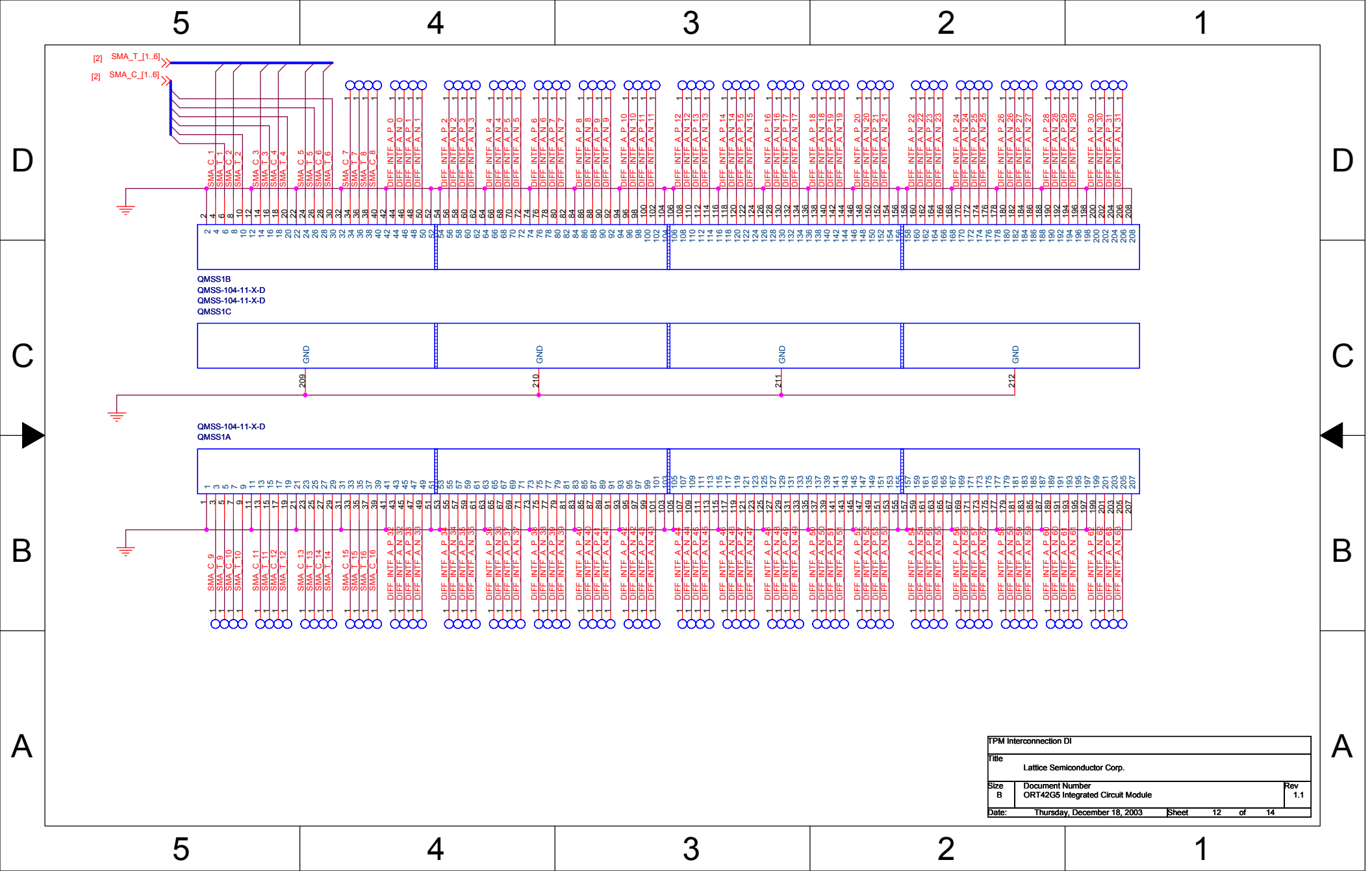




TPM Interconnection B		
Title		
Lattice Semiconductor Corp.		
Size	Document Number	Rev
B	ORT42G5 Integrated Circuit Module	1.1
Date:	Thursday, December 18, 2003	Sheet 10 of 14



TPM Interconnection C		
Title		
Lattice Semiconductor Corp.		
Size	Document Number	Rev
B	ORT42G5 Integrated Circuit Module	1.1
Date:	Thursday, December 18, 2003	Sheet 11 of 14



TPM Interconnection DI		
Title		
Lattice Semiconductor Corp.		
Size	Document Number	Rev
B	ORT42G5 Integrated Circuit Module	1.1
Date: Thursday, December 18, 2003		
Sheet 12 of 14		

