



NOTES: UNLESS OTHERWISE SPECIFIED

1. DIMENSIONS AND TOLERANCES PER ANSI Y14.5M.

2. ALL DIMENSIONS ARE IN MILLIMETERS.

3. DIMENSION "b" IS MEASURED AT THE MAXIMUM SOLDER BALL DIAMETER, PARALLEL TO PRIMARY DATUM C.

4. PRIMARY DATUM C AND SEATING PLANE ARE DEFINED BY THE SPHERICAL CROWNS OF THE SOLDER BALLS.

5. BILATERAL TOLERANCE ZONE IS APPLIED TO EACH SIDE OF THE PACKAGE BODY.

6. EXACT SHAPE AND SIZE OF THIS FEATURE IS OPTIONAL.

7. JEDEC REFERENCE: JEP95 DG4.18

	SYMBOL	COMMON DIMENSIONS		
		MIN.	NOM.	MAX.
Total Thickness	A	0.507	0.546	0.585
Stand Off	A1	0.169	0.199	0.229
Wafer Thickness	A2	0.322	±0.025	
Backside Coating Thickness	A3	0.025		
Body Size	D	3.190	BSC	
	E	3.102	BSC	
Ball Diameter (Size)		0.250		
Ball/Bump Width	b	0.200	0.250	0.300
Ball/Bump Pitch	eD	0.400		
	eE	0.400		
Ball/Bump Count	n	49		
Edge Ball Center to Center	D1	2.400	BSC	
	E1	2.400	BSC	
Package Edge Tolerance	aaa	0.030		
Coplanarity (whole wafer)	ddd	0.050		
Ball/Bump Offset (Package)	eee	0.015		

Title: Package Outline Drawing

Pkg Type: WLCSP

Document No:

Product Family: LFMXO4

Pin Count: 49

POD-250074

Product Name: LFMXO4-025-UUG49

Pkg Size: 3.190x3.102 mm

Rev: A

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