



# **MachXO4 PLL Module**

IP Version: v3.0.1

## **Release Notes**

FPGA-RN-02100-1.0

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# 1. Introduction

This document contains the Release Notes for the PLL Module. For specific details about the IP, refer to the following:

- [MachXO4 PLL Module User Guide \(FPGA-IPUG-02316\)](#)

## PLL Module v3.0.1

Software	Software Version	Summary of Changes
Lattice Radiant	2025.2	<ul style="list-style-type: none"><li>• Fixed output enable port functionality.</li><li>• Fixed and enhanced testbench.</li></ul>

## PLL Module Earlier Versions

IP Version	Summary of Changes
3.0.0	Initial release for LFMXO4.

## References

- [MachXO4 PLL Module User Guide \(FPGA-IPUG-02316\)](#)
- [MachXO4](#) web page
- [Lattice Radiant Software](#) web page
- [Lattice Propel Design Environment](#) web page
- [Lattice Solutions IP Cores](#) web page
- [Lattice Solutions Reference Designs](#) web page
- [Lattice Insights](#) for Lattice Semiconductor training courses and learning plans

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