



MachXO4 DDR 7:1 Module

IP Version: v3.0.0

Release Notes

FPGA-RN-02099-1.0

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1. Introduction

This document contains the Release Notes for the GDDR 7:1 I/O Module. For specific details about the IP, refer to the following:

- [MachXO4 DDR 7:1 Module \(FPGA-IPUG-02315\)](#)

GDDR 7:1 I/O Module v3.0.0

Software	Software Version	Summary of Changes
Lattice Radiant™	2025.2	Initial release and support for MachXO4 (LFMXO4).

References

- [MachXO4 DRR 7:1 Module User Guide \(FPGA-IPUG-02315\)](#)
- [MachXO4](#) web page
- [Lattice Radiant Software](#) web page
- [Lattice Propel Design Environment](#) web page
- [Lattice Solutions IP Cores](#) web page
- [Lattice Solutions Reference Designs](#) web page
- [Lattice Insights](#) for Lattice Semiconductor training courses and learning plans

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For frequently asked questions, refer to the Lattice Answer Database at www.latticesemi.com/Support/AnswerDatabase.



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