



LPDDR4 Memory Interface Module

IP Version: v1.5.0

Release Notes

FPGA-RN-02103-1.0

December 2025

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1. Introduction

This document contains the Release Notes for the LPDDR4 Memory Interface Module IP. For specific details about the IP, refer to the following:

- [LPDDR4 Memory Interface Module IP User Guide \(FPGA-IPUG-02154\)](#)

LPDDR4 Memory Interface Module IP v1.5.0

Software	Software Version	Summary of Changes
Lattice Radiant™	2025.2	<ul style="list-style-type: none"> • Added support for Lattice Propel. • Changed the SIM_FLOAT_PRECISION parameter for the internal PLL to 0.625, a setting required for the PLL to lock successfully when used with another PLL driving the REFCK during simulation.

LPDDR4 Memory Interface Module IP Earlier Versions

IP Version	Summary of Changes
1.4.0	<ul style="list-style-type: none"> • Added LFCPNX-100AUTODIE support. • Added LFMXO5-55T support.
1.3.0	<ul style="list-style-type: none"> • Added UT24CP support. • Added LFMXO5-100T support.
1.2.0	<ul style="list-style-type: none"> • Added Radiant 3.1 support. • Updated calculation of PLL settings. • Updated file hierarchy in testbench.
1.1.0	<ul style="list-style-type: none"> • Added Radiant 3.0 support. • Updated PLL instance.
1.0.0	<ul style="list-style-type: none"> • Initial release.

References

- [LPDDR4 Memory Interface Module IP User Guide \(FPGA-IPUG-02154\)](#)
- [CertusPro-NX](#) web page
- [IP Core](#) for CertusPro-NX
- [Lattice Radiant Software](#) web page
- [Lattice Propel Design Environment](#) web page
- [Lattice Insights](#) for Lattice Semiconductor training courses and learning plans

Technical Support Assistance

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For frequently asked questions, please refer to the Lattice Answer Database at www.latticesemi.com/Support/AnswerDatabase.



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