



## **Debayer IP**

IP Version: v1.4.0

## **Release Notes**

FPGA-RN-02054-1.1

December 2025

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# 1. Introduction

This document contains the Release Notes for the Debayer IP. For specific details about the IP, refer to the following:

- [Debayer IP User Guide \(FPGA-IPUG-02203\)](#)

## Debayer IP v1.4.0

Software	Software Version	Summary of Changes
Lattice Radiant	2025.2	<ul style="list-style-type: none"> <li>• Added support for Certus-N2 devices.</li> <li>• Updated constraint files.</li> <li>• Removed IP license requirement.</li> </ul>

## Debayer IP v1.3.0

Software	Software Version	Summary of Changes
Lattice Radiant	2025.1	Added support for LFD2NX-25 and LFD2NX-28 devices.

## Debayer IP Earlier Versions

IP Version	Summary of Changes
1.2.2	Fixed combinational loop issue.
1.2.1	<ul style="list-style-type: none"> <li>• Removed license strings.</li> <li>• Fixed an issue in proper handling of pixels per clock (PPC) during vertical blanking period.</li> <li>• Fixed an issue in bus width computation of AXI4-Stream receiver data.</li> <li>• Fixed an issue in TVALID behavior during vertical blanking.</li> <li>• Fixed testbench related issues.</li> </ul>
1.2.0	<ul style="list-style-type: none"> <li>• Fixed issues in bus interface connectivity.</li> <li>• Fixed issues in handling TVALID going LOW during vertical blanking period.</li> <li>• Other minor IP enhancements.</li> </ul>
1.1.0	<ul style="list-style-type: none"> <li>• Added support for Lattice Avant devices.</li> <li>• Timing improvements and resource utilization optimizations.</li> <li>• Added support for partial resolution.</li> <li>• Added regression support in Verilog testbench.</li> </ul>
1.0.0	Initial release.

## References

- [Debayer IP User Guide \(FPGA-IPUG-02203\)](#)
- [CrossLink-NX](#) web page
- [Certus-NX](#) web page
- [Certus-N2](#) web page
- [CertusPro-NX](#) web page
- [Avant-E](#) web page
- [Avant-G](#) web page
- [Avant-X](#) web page
- [Lattice Propel Design Environment](#) web page
- [Lattice Radiant Software](#) web page
- [Lattice Insights](#) for Lattice Semiconductor training courses and learning plans

## Technical Support Assistance

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For frequently asked questions, refer to the Lattice Answer Database at [www.latticesemi.com/Support/AnswerDatabase](http://www.latticesemi.com/Support/AnswerDatabase).



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