



I/O Interface IP Release Notes

IP Version: v1.1.0

Release Notes

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This document was created consistent with Lattice Semiconductor's inclusive language policy. In some cases, the language in underlying tools and other items may not yet have been updated. Please refer to Lattice's inclusive language [FAQ 6878](#) for a cross reference of terms. Note in some cases such as register names and state names it has been necessary to continue to utilize older terminology for compatibility.

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1. Introduction

This document contains the Release Notes for the I/O Interface IP. For specific details about the IP, refer to the following:

- [I/O Interface IP User Guide \(FPGA-IPUG-02292\)](#)

I/O Interface IP v1.1.0

Software	Software Version	Summary of Changes
Lattice Radiant	2025.2	Added support for all devices in the Lattice Radiant software.

I/O Interface IP v1.0.0

Software	Software Version	Summary of Changes
Lattice Radiant	2025.1	Initial release.

References

- [I/O Interface IP User Guide \(FPGA-IPUG-02292\)](#)
- [Lattice Certus-N2 Family Devices](#) web page
- [Lattice Avant-E Family Devices](#) web page
- [Lattice Avant-G Family Devices](#) web page
- [Lattice Avant-X Family Devices](#) web page
- [MachXO5-NX Family Devices](#) web page
- [Certus-NX Family Devices](#) web page
- [CertusPro-NX Family Devices](#) web page
- [Lattice Radiant Software](#) web page
- [Lattice Insights](#) for Lattice Semiconductor training courses and learning plans

Technical Support Assistance

Submit a technical support case through www.latticesemi.com/techsupport.

For frequently asked questions, please refer to the Lattice Answer Database at www.latticesemi.com/Support/AnswerDatabase.



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