DisplayPort IP

IP Version: v2.2.0

Release Notes

FPGA-RN-02071-1.3

June 2026

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Contents

1. Introduction

This document contains the Release Notes for the DisplayPort IP. For specific details about the IP, refer to the following:

- DisplayPort IP User Guide (FPGA-IPUG-02236)
- DisplayPort Driver API Reference (FPGA-TN-02423)

DisplayPort IP v2.2.0

Software	Software Version	Summary of Changes
Lattice Radiant Lattice Propel	2026.1	• Added support for Lattice Avant-G/X and Certus-N2 devices. • Removed the s_axi_aresetn_i port.

DisplayPort IP v2.1.1

Software	Software Version	Summary of Changes
Lattice Radiant Lattice Propel	2025.2.1	• Fixed the pll_lock connection in the top.sv file of the hardware example design to ensure the correct I2C clock configuration is set. • Resolved the issue to enable RX functionality for certain resolutions.

DisplayPort IP v2.1.0

Software	Software Version	Summary of Changes
Lattice Radiant Lattice Propel	2025.2	• Added support for YCbCr422 color format. • Added support for optional IP registers with AXI-Lite interface. • Added support for 6 bits per color (BPC). • Added color format selection default to Tx Video Settings in GUI. • Added support for dynamically updating MSA field. • Added support for dynamic reconfiguration for data rate and lane switching. • Added support for Lattice Propel software. • Updated example design to demonstrate software support. • Reduced EBR usage for RX. • Added support for LFCPNX-50 devices. • Driver release v25.2.0.

DisplayPort IP v2.0.0

Software	Software Version	Summary of Changes
Lattice Radiant	2025.1	• Enhanced support for small size DisplayPort IP. • Made interface and parameter changes that are not backward compatible. • Extended support for data rate: RBR, HBR, HBR2, HBR3. • Updated support for bits per color (BPC): 8, 10, 12, 16. • Updated support for static data rate and resolution.

DisplayPort IP Earlier Versions

IP Version	Summary of Changes
1.2.0	• Updated for the Lattice Radiant software 2024.1. • Bug fixes on hardware robustness testing.

IP Version	Summary of Changes
	• Updated DisplayPort IP registers. • Added new user_clk port.
1.1.0	• Initial release for the Lattice Radiant software 2023.2. • Added support for data rate: RBR, HBR, HBR2. • Added support for lane count: 1, 2, 4. • Added support for bits per color (BPC): 6, 8, 10, 12, 16. • Added support for colorimetry format: RGB, YCbCr444. • Added support for DisplayPort IP mode: SST. • Added support for static data rate and resolution.

References

- DisplayPort IP User Guide (FPGA-IPUG-02236)
- DisplayPort Driver API Reference (FPGA-TN-02423)
- CertusPro-NX web page
- Certus-N2 web page
- Avant-G web page
- Avant-X web page
- DisplayPort IP Core web page
- Lattice Propel Design Environment web page
- Lattice Radiant Software web page
- Lattice Insights for Lattice Semiconductor training courses and learning plans

Technical Support Assistance

Submit a technical support case through www.latticesemi.com/techsupport.

For frequently asked questions, refer to the Lattice Answer Database at www.latticesemi.com/Support/AnswerDatabase.

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