

FFT Compiler IP

IP Version: v1.6.0

Release Notes

FPGA-RN-02069-1.0

December 2025



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1. Introduction

This document contains the Release Notes for the FFT Compiler IP. For specific details about the IP, refer to the following:

• FFT Compiler IP User Guide (FPGA-IPUG-02153)

FFT Compiler IP v1.6.0

Software	Software Version	Summary of Changes
Lattice Radiant Lattice Propel Builder	2025.2	Added the High Performance Radix-4 architecture with support for block floating-point scaling.
		Improved output accuracy of the IP core.
		Resolved issue with dynamic FFT point configuration when using a variable number of FFT points.
		Corrected GUI display of parameter dependencies.
		Removed the IP licensing requirement.

FFT Compiler IP Earlier Versions

IP Version	Summary of Changes		
1.5.0	Added support for the Lattice Propel software.		
	Added support for Certus-N2 devices.		
1.4.0	Added support for Lattice Avant devices for High Performance Architecture.		
1.3.0	Added support for Lattice Avant, Certus-NX-RT, and CertusPro-NX-RT devices.		
1.2.0	Added support for MachXO5-NX devices.		
1.1.0	Added support for CertusPro-NX devices.		
1.0.0	Initial release.		



References

- FFT Compiler IP User Guide (FPGA-IPUG-02153)
- Avant-E web page
- Avant-G web page
- Avant-X web page
- Certus-N2 web page
- Certus-NX web page
- CertusPro-NX web page
- CrossLink-NX web page
- MachXO5-NX web page
- FFT Compiler IP Core web page
- Lattice Propel Design Environment web page
- Lattice Radiant Software web page
- Lattice Solutions IP Cores web page
- Lattice Solutions Reference Designs web page
- Lattice Insights web page for Lattice Semiconductor training courses and learning



Technical Support Assistance

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www.latticesemi.com/Support/AnswerDatabase.



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