Lattice Sentry I2C Filter IP

IP Version: v1.4.0

Release Notes

FPGA-RN-02070-1.0

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1. Introduction

This document contains the Release Notes for the Lattice Sentry I2C Filter IP. For specific details about the IP, refer to the following:

- Lattice Sentry I2C Filter IP User Guide (FPGA-IPUG-02166)

Lattice Sentry I2C Filter IP v1.4.0

Software	Software Version	Summary of Changes
Lattice Radiant Lattice Propel Builder	2025.2	• Added support for MachXO4 devices. • Updated GUI to remove tool selection.

Lattice Sentry I2C Filter IP Earlier Versions

IP Version	Summary of Changes
1.3.0	Fixed glitch filter when clock stretching is enabled.
1.2.0	Fixed handling of clock stretching.
1.1.0	• Added SCL speed select port to control output SCL frequency. • Improved clock stretching feature. • Added support for MachXO5-NX devices.
1.0.0	Initial release.

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References

- Lattice Sentry I2C Filter IP User Guide (FPGA-IPUG-02166)
- Mach-NX web page
- MachXO3D web page
- MachXO4 web page
- MachXO5-NX web page
- Lattice Sentry I2C Filter IP Core web page
- Lattice Diamond Software web page
- Lattice Propel Design Environment web page
- Lattice Radiant Software web page
- Lattice Solutions IP Cores web page
- Lattice Solutions Reference Designs web page
- Lattice Insights web page for Lattice Semiconductor training courses and learning

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