Flash Access IP

IP Version: v2.2.0

Release Notes

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Contents

1.   Introduction

This document contains the Release Notes for the Flash Access IP. For specific details about the IP, refer to the following:

- Flash Access IP User Guide (FPGA-IPUG-02171)

Flash Access IP v2.2.0

Software	Software Version	Summary of Changes
Lattice Radiant	2025.1	Added support for LFMXO5-35, LFMXO5-35T, LFMXO5-65, and LFMXO5-65T devices.

Flash Access IP v2.1.0

Software	Software Version	Summary of Changes
Lattice Radiant	2024.2	Added *Config LMMI Interface Enable* attribute for all supported devices.

Flash Access IP Earlier Versions

IP Version	Summary of Changes
2.0.1	Updated RTL to resolve combinational loop warning flagged by Radiant SW during place and route.
2.0.0	Added LFMXO5-100T and LFMXO5-55T support.
1.1.0	Updated IP package with driver.
1.0.2	Updated frequency of the internal LMMI CLK, limiting to 50 MHz.
1.0.1	Updated design for on-chip flash erase function.
1.0.0	Initial release.

4   FPGA-RN-02064-1.1

References

- Flash Access IP User Guide (FPGA-IPUG-02171)
- MachXO5-NX web page
- Flash Access IP Core web page
- Lattice Radiant Software web page
- Lattice Solutions IP Cores web page
- Lattice Insights for Lattice Semiconductor training courses and learning plans

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