



SPI Flash Memory Controller IP

IP Version: v2.0.0

Release Notes

FPGA-RN-02039-1.0

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1. Introduction

This document contains the Release Notes for the SPI Flash Memory Controller IP. For specific details about the IP, refer to the following:

- [SPI Flash Memory Controller IP User Guide \(FPGA-IPUG-02134\)](#)

Note: These release notes document the changes from IP version 2.0.0 and Lattice Radiant software version 2024.2 and above.

SPI Flash Memory Controller IP v2.0.0

Software	Software Version	Summary of Changes
Lattice Radiant	2024.2	Added LFD2NX-28 and LFD2NX-9 device support.

SPI Flash Memory Controller IP v1.9.0 and Below

Software	Software Version	Summary of Changes
Lattice Radiant	2024.1 and below	Refer to the Release Notes section in <i>introduction.html</i> in the Radiant software installation folder or the IP server for details on changes made in previous versions of this IP.

References

- [SPI Flash Memory Controller IP User Guide \(FPGA-IPUG-02134\)](#)
- [Avant-E](#) web page
- [Avant-G](#) web page
- [Avant-X](#) web page
- [Certus-N2](#) web page
- [Certus-NX](#) web page
- [CertusPro-NX](#) web page
- [CrossLink-NX](#) web page
- [MachXO5-NX](#) web page
- [SPI Flash Memory Controller IP Core](#) web page
- [Lattice Propel Builder](#) software
- [Lattice Radiant](#) FPGA design software
- [Lattice Insights](#) for Lattice Semiconductor training courses and learning plans

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For frequently asked questions, please refer to the Lattice Answer Database at www.latticesemi.com/Support/AnswerDatabase



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