



OpenLDI/FPD-Link/LVDS Transmitter IP

IP Version: v1.8.0

Release Notes

FPGA-RN-02012-1.2

December 2025

Disclaimers

Lattice makes no warranty, representation, or guarantee regarding the accuracy of information contained in this document or the suitability of its products for any particular purpose. All information herein is provided AS IS, with all faults, and all associated risk is the responsibility entirely of the Buyer. The information provided herein is for informational purposes only and may contain technical inaccuracies or omissions, and may be otherwise rendered inaccurate for many reasons, and Lattice assumes no obligation to update or otherwise correct or revise this information. Products sold by Lattice have been subject to limited testing and it is the Buyer's responsibility to independently determine the suitability of any products and to test and verify the same. LATTICE PRODUCTS AND SERVICES ARE NOT DESIGNED, MANUFACTURED, OR TESTED FOR USE IN LIFE OR SAFETY CRITICAL SYSTEMS, HAZARDOUS ENVIRONMENTS, OR ANY OTHER ENVIRONMENTS REQUIRING FAIL-SAFE PERFORMANCE, INCLUDING ANY APPLICATION IN WHICH THE FAILURE OF THE PRODUCT OR SERVICE COULD LEAD TO DEATH, PERSONAL INJURY, SEVERE PROPERTY DAMAGE OR ENVIRONMENTAL HARM (COLLECTIVELY, "HIGH-RISK USES"). FURTHER, BUYER MUST TAKE PRUDENT STEPS TO PROTECT AGAINST PRODUCT AND SERVICE FAILURES, INCLUDING PROVIDING APPROPRIATE REDUNDANCIES, FAIL-SAFE FEATURES, AND/OR SHUT-DOWN MECHANISMS. LATTICE EXPRESSLY DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY OF FITNESS OF THE PRODUCTS OR SERVICES FOR HIGH-RISK USES. The information provided in this document is proprietary to Lattice Semiconductor, and Lattice reserves the right to make any changes to the information in this document or to any products at any time without notice.

Inclusive Language

This document was created consistent with Lattice Semiconductor's inclusive language policy. In some cases, the language in underlying tools and other items may not yet have been updated. Please refer to Lattice's inclusive language [FAQ 6878](#) for a cross reference of terms. Note in some cases such as register names and state names it has been necessary to continue to utilize older terminology for compatibility.

Contents

Contents	3
1. Introduction	4
OpenLDI/FPD-Link/LVDS Transmitter IP v1.8.0	4
OpenLDI/FPD-Link/LVDS Transmitter IP v1.7.0	4
OpenLDI/FPD-Link/LVDS Transmitter IP v1.6.0	4
OpenLDI/FPD-Link/LVDS Transmitter IP Earlier Versions	4
References	5
Technical Support Assistance	6

1. Introduction

This document contains the Release Notes for the FPD-Link Transmitter IP. For specific details about the IP, refer to the following:

- [OpenLDI/FPD-Link/LVDS Transmitter IP User Guide \(FPGA-IPUG-02117\)](#)

OpenLDI/FPD-Link/LVDS Transmitter IP v1.8.0

Software	Software Version	Summary of Changes
Lattice Radiant™	2025.2	<ul style="list-style-type: none">• Updated licensing by removing IP-specific license strings. The FPD-Link Transmitter IP is now provided at no additional cost with the Lattice Radiant software.• Updated customer testbench to fix data mismatch simulation error.• Updated constraint file.• Updated RTL for CSR bug fix.

OpenLDI/FPD-Link/LVDS Transmitter IP v1.7.0

Software	Software Version	Summary of Changes
Lattice Radiant	2025.1	Added support for LFD2NX-15 and LFD2NX-25 devices.

OpenLDI/FPD-Link/LVDS Transmitter IP v1.6.0

Software	Software Version	Summary of Changes
Lattice Radiant	2024.2	<ul style="list-style-type: none">• Added support for Certus™-N2 devices.• Added support for LFD2NX-9 and LFD2NX-28 devices.

OpenLDI/FPD-Link/LVDS Transmitter IP Earlier Versions

IP Version	Summary of Changes
1.5.0	Added Certus-N2 support (early release).
1.4.0	<ul style="list-style-type: none">• Added Radiant 2024.1 support.• Added support for Unified Video Streaming interface.• Added Data type Dynamic Reconfiguration feature.• Added support for AXI4-Lite interface.
1.3.1	<ul style="list-style-type: none">• Updated Avant™ primitives for Radiant 2024.2 compatibility.• Fixed design and testbench issue related to JEIDA format pixel mapping.
1.3.0	<ul style="list-style-type: none">• Added LFD2NX-17, LIFCL, and LAV-AT support.• Added Propel™ support.• Added AXI4-Stream Interface.• Testbench enhancement for AXI4-Stream configuration.
1.2.0	Added LFMX05-25 support.
1.1.0	<ul style="list-style-type: none">• Added support for JEIDA/VESA mode.• Added CertusPro™-NX support.
1.0.1	Testbench enhancement.
1.0.0	Initial release.

References

- [OpenLDI/FPD-Link/LVDS Transmitter IP User Guide \(FPGA-IPUG-02117\)](#)
- [Avant-E web page](#)
- [Avant-G web page](#)
- [Avant-X web page](#)
- [Certus-N2 web page](#)
- [Certus-NX web page](#)
- [CertusPro-NX web page](#)
- [CrossLink-NX web page](#)
- [MachXO5-NX web page](#)
- [FPD-Link Transmitter IP Core web page](#)
- [Lattice Propel Builder software](#)
- [Lattice Radiant FPGA design software](#)
- [Lattice Insights](#) for Lattice Semiconductor training courses and learning plans

Technical Support Assistance

Submit a technical support case through www.latticesemi.com/techsupport.

For frequently asked questions, refer to the Lattice Answer Database at
www.latticesemi.com/Support/AnswerDatabase



www.latticesemi.com