



OpenLDI/FPD-Link/LVDS Receiver IP

IP Version: v1.9.0

Release Notes

FPGA-RN-02013-1.2

December 2025

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1. Introduction

This document contains the Release Notes for the FPD-Link Receiver IP. For specific details about the IP, refer to the following:

- [OpenLDI/FPD-Link/LVDS Receiver IP User Guide \(FPGA-IPUG-02116\)](#)

OpenLDI/FPD-Link/LVDS Receiver IP v1.9.0

Software	Software Version	Summary of Changes
Lattice Radiant™	2025.2	<ul style="list-style-type: none"> • Updated licensing by removing IP-specific license strings. The FPD-Link Receiver IP is now provided at no additional cost with the Lattice Radiant software. • Updated constraint file.

OpenLDI/FPD-Link/LVDS Receiver IP v1.8.0

Software	Software Version	Summary of Changes
Lattice Radiant	2025.1	<ul style="list-style-type: none"> • Added support for LFD2NX-15, LFD2NX-25, LFD2NX-35, and LFD2NX-65 devices. • Added support for LFMXO5-35, LFMXO5-35T, LFMXO5-65, and LFMXO5-65T devices.

OpenLDI/FPD-Link/LVDS Receiver IP v1.7.0

Software	Software Version	Summary of Changes
Lattice Radiant	2024.2	<ul style="list-style-type: none"> • Added support for LN2-CT-20ES device. • Added support for LFD2NX-9 and LFD2NX-28 devices.

OpenLDI/FPD-Link/LVDS Receiver IP Earlier Versions

IP Version	Summary of Changes
1.6.0	Added Certus™-N2 support (early release).
1.5.0	<ul style="list-style-type: none"> • Added Radiant 2024.1 support. • Added support for AXI4-Lite Interface protocol and AXI4-Stream Protocol through Unified Video Streaming TX Interface. • Added Data Type dynamic reconfiguration feature.
1.4.1	Added Radiant 2023.2 support.
1.4.0	Added Avant device support.
1.3.0	<ul style="list-style-type: none"> • Integrated 1.2.1 updates to 1.3.0. • Added LFMXO5 device support.
1.2.1	Updated generic PLL for Radiant 3.0.
1.2.0	Added CertusPro™-NX support.
1.1.0	<ul style="list-style-type: none"> • Added support for JEIDA/VESA mode. • Added support for LFCPNX device.
1.0.1	Testbench enhancement.
1.0.0	Initial release.

References

- [OpenLDI/FPD-Link/LVDS Receiver IP User Guide \(FPGA-IPUG-02116\)](#)
- [Avant-E](#) web page
- [Avant-G](#) web page
- [Avant-X](#) web page
- [Certus-N2](#) web page
- [Certus-NX](#) web page
- [CertusPro-NX](#) web page
- [CrossLink-NX](#) web page
- [MachXO5-NX](#) web page
- [OpenLDI/FPD-Link/LVDS Receiver IP Core](#) web page
- [Lattice Radiant Software](#) web page
- [Lattice Solutions IP Cores](#) web page
- [Lattice Solutions Reference Designs](#) web page
- [Lattice Insights](#) for Lattice Semiconductor training courses and learning plans

Technical Support Assistance

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For frequently asked questions, refer to the Lattice Answer Database at www.latticesemi.com/Support/AnswerDatabase



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