Color Correction Matrix (CCM) IP

IP Version: v1.3.2

Release Notes

FPGA-RN-02030-1.2

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1. Introduction

This document contains the Release Notes for the Color Correction Matrix (CCM) IP. For specific details about the IP, refer to the following:

- Color Correction Matrix (CCM) IP User Guide (FPGA-IPUG-02214)

Color Correction Matrix (CCM) IP v1.3.2

Software	Software Version	Summary of Changes
Lattice Radiant	2026.1	• Added support for LFD2NX-25P device. • Updated clock frequency constraints.

Color Correction Matrix (CCM) IP v1.3.1

Software	Software Version	Summary of Changes
Lattice Radiant	2025.2	• Added support for Certus-N2 devices. • Removed the IP licensing requirement. • Updated the constraint files. • Updated the AXI-Lite write strobe bus interface.

Color Correction Matrix (CCM) IP v1.3.0

Software	Software Version	Summary of Changes
Lattice Radiant	2025.1	Added support for LFD2NX-25 and LFD2NX-28 devices.

Color Correction Matrix (CCM) IP Earlier Versions

IP Version	Summary of Changes
1.2.1	• Fixed an issue with proper handling of PPC during the vertical blanking period. • Fixed testbench-related issues.
1.2.0	• Fixed bus interface connectivity issues. • Fixed incorrect assertion of the End of Frame output port. • Fixed issues with handling *TVALID* going *LOW* during the vertical blanking period. • Implemented other minor IP enhancements.
1.1.0	• Added support for the Lattice Avant devices. • Improved timing and optimized resource utilization. • Added support for partial resolution. • Added regression support in the Verilog testbench.
1.0.0	Initial release.

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References

- Color Correction Matrix (CCM) IP User Guide (FPGA-IPUG-02214)
- Avant-E web page
- Avant-G web page
- Avant-X web page
- Certus-N2 web page
- Certus-NX web page
- CertusPro-NX web page
- CrossLink-NX web page
- Lattice Propel Design Environment web page
- Lattice Radiant Software web page
- Lattice Insights web page for Lattice Semiconductor training courses and learning plans

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