USB 2.0/3.2 IP

IP Version: v3.2.0

Release Notes

FPGA-RN-02037-1.3

October 2025

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Contents

1. Introduction

This document contains the Release Notes for the USB 2.0/3.2 IP. For specific details about the IP, refer to the following:

- USB 2.0/3.2 IP User Guide (FPGA-IPUG-02237)

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Software	Software Version	Summary of Changes
Lattice Radiant	2025.1.1	• Updated the reference design to remove IN controller memory and rready monitor instance from the design top file and module. • Updated the demo to add single bit file used to enumerate and stream data in either HS or SS mode based on the host port that it is connected to. • Fixed software issues affecting data transfers larger than 2 MB.

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Software	Software Version	Summary of Changes
Lattice Radiant	2025.1	Removed IP license control.

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Software	Software Version	Summary of Changes
Lattice Radiant	2024.2.1	USB3 throughput improvement from ~1 Gbps to ~3 Gbps.

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Software	Software Version	Summary of Changes
Lattice Radiant	2024.2	• Example design support for CTRL, INTR, BULK, and ISOC traffic support. • AHB-Lite interface support. • Modular firmware API support.

USB 2.0/3.2 IP Earlier Versions

IP Version	Summary of Changes
2.0.0	• Added USB 3.0 Super Speed features. • USB23 Example Design is moved to eval folder.
1.0.0	Initial release with USB2 support only.

References

- USB 2.0/3.2 IP User Guide (FPGA-IPUG-02237)
- CrossLink-NX web page
- Lattice Solutions IP Cores web page
- Lattice Solutions Reference Designs web page
- Lattice Solutions Boards web page
- Lattice Solutions Demonstrations web page
- Lattice Propel Builder software web page
- Lattice Radiant FPGA design software web page
- Lattice Insights for Lattice Semiconductor training courses and learning plans

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For frequently asked questions, refer to the Lattice Answer Database at
www.latticesemi.com/Support/AnswerDatabase