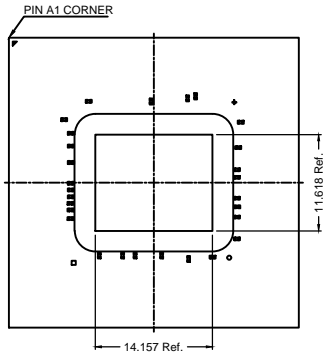
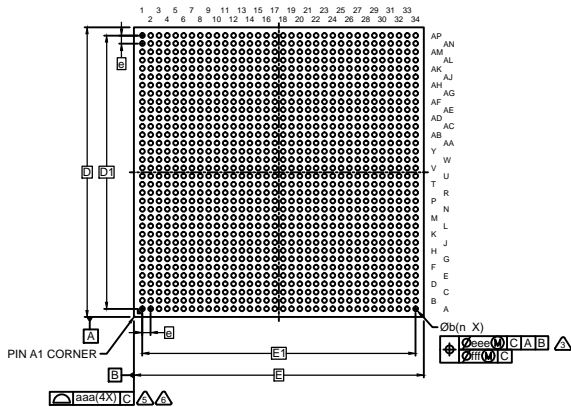
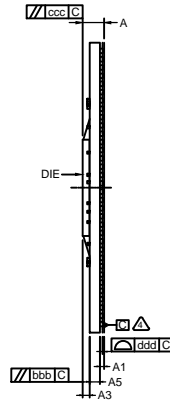




TOP VIEW



SIDE VIEW



BOTTOM VIEW

	Symbol	Common Dimensions		
		MIN.	NOM.	MAX.
Total Thickness:	A	2.423	2.605	2.787
Stand Off:	A1	0.400	0.500	0.600
Substrate Thickness:	A5	1.258	Ref.	
THICKNESS FROM SUBSTRATE SURFACE TO THE HIGHEST POINT:	A3	0.847	Ref.	
Body Size:	X	E	35.000	BSC.
	Y	D	35.000	BSC.
Ball Diameter:			0.600	
Ball Width:	b	0.500	0.600	0.700
Ball Pitch:	e		1.000	
Ball Count:	n		1156	
Edge Ball Center to Center:	X	E1	33.000	
	Y	D1	33.000	
Package Edge Tolerance:	aaa		0.200	
Substrate Parallelism:	bbb		0.250	
Top Parallelism:	ccc		0.200	
Coplanarity:	ddd		0.200	
Ball Offset (Package):	eee		0.250	
Ball Offset (Ball):	fff		0.100	

NOTES: UNLESS OTHERWISE SPECIFIED

- DIMENSIONS AND TOLERANCES PER ANSI Y14.5M.
- ALL DIMENSIONS ARE IN MILLIMETERS.
- DIMENSION "b" IS MEASURED AT THE MAXIMUM SOLDER BALL DIAMETER, PARALLEL TO PRIMARY DATUM \square .
- PRIMARY DATUM \square AND SEATING PLANE ARE DEFINED BY THE SPHERICAL CROWNS OF THE SOLDER BALLS.
- BILATERAL TOLERANCE ZONE IS APPLIED TO EACH SIDE OF THE PACKAGE BODY.
- EXACT SHAPE AND SIZE OF THIS FEATURE IS OPTIONAL.
- JEDEC REFERENCE: JEP95 DR4.14

Title: Package Outline Drawing

Pkg Type: FCBGA

Document No:

Product Family: LAV-AT

Pkg I/O Count: 1156

POD-240042

Product Name: LAV-AT-70-LFG1156
LAV-AT-70-LFE1156

Pkg Size: 35x35 mm

Rev: B

PAGE: 3/3

Disclaimers

Lattice makes no warranty, representation, or guarantee regarding the accuracy of information contained in this document or the suitability of its products for any particular purpose. All information herein is provided AS IS, with all faults, and all associated risk is the responsibility entirely of the Buyer. The information provided herein is for informational purposes only and may contain technical inaccuracies or omissions, and may be otherwise rendered inaccurate for many reasons, and Lattice assumes no obligation to update or otherwise correct or revise this information. Products sold by Lattice have been subject to limited testing and it is the Buyer's responsibility to independently determine the suitability of any products and to test and verify the same. LATTICE PRODUCTS AND SERVICES ARE NOT DESIGNED, MANUFACTURED, OR TESTED FOR USE IN LIFE OR SAFETY CRITICAL SYSTEMS, HAZARDOUS ENVIRONMENTS, OR ANY OTHER ENVIRONMENT REQUIRING FAIL-SAFE PERFORMANCE, INCLUDING ANY APPLICATION IN WHICH THE FAILURE OF THE PRODUCT OR SERVICE COULD LEAD TO DEATH, PERSONAL INJURY, SEVERE PROPERTY DAMAGE OR ENVIRONMENTAL HARM (COLLECTIVELY, "HIGH-RISK USES"). FURTHER, BUYER MUST TAKE PRUDENT STEPS TO PROTECT AGAINST PRODUCT AND SERVICE FAILURES, INCLUDING PROVIDING APPROPRIATE REDUNDANCIES, FAIL-SAFE FEATURES, AND/OR SHUT-DOWN MECHANISMS. LATTICE EXPRESSLY DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY OF FITNESS OF THE PRODUCTS OR SERVICES FOR HIGH-RISK USES.

The information provided in this document is proprietary to Lattice Semiconductor, and Lattice reserves the right to make any changes to the information in this document or to any products at any time without notice.