JESD204B IP

IP Version: v1.4.0

Release Notes

FPGA-RN-02006-1.4

June 2026

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Contents

1. Introduction

This document contains the Release Notes for the JESD204B IP. For specific details about the IP and driver, refer to the following:

- JESD204B IP User Guide (FPGA-IPUG-02259)
- JESD204B Driver API Reference (FPGA-TN-02412)

JESD204B IP v1.4.0

Software	Software Version	Summary of Changes
Lattice Radiant™ Lattice Propel™	2026.1	• Added support for LN2-CT-20 device. • Fixed an issue in the Module/IP Block Wizard that allowed invalid IP parameter configurations to be generated when *High Density Format (HD)* is disabled.

JESD204B IP v1.3.0

Software	Software Version	Summary of Changes

Lattice Radiant
Lattice Propel
	2025.2	• Added support for Certus™-N2 devices (except LN2-CT-20ES).
		• Enabled support for deterministic latency.
		• Enabled support for continuous data rate on Avant™-G and Avant-X devices.
		• Added Rx register offset 0x34 (buffer fill level register).
		• Driver release v25.02.00:
		  • Restructured all driver APIs for improved readability and clarity.
		  • Added buffer fill level register.

Corrections to Previous Release Notes

- All IP versions listed below are supported in the Lattice Propel Builder software.

JESD204B IP v1.2.0

Software	Software Version	Summary of Changes
Lattice Radiant	2025.1	• Added constraint to IP configuration whereby the value of F × K ((Octets per Frame + 1) × (Frames per Multiframe + 1)) must be a multiple of 4. • Added hardware example designs for Avant-X Versa Board. • Initial driver release v25.01.00.

JESD204B IP v1.1.0

Software	Software Version	Summary of Changes
Lattice Radiant	2024.2.1	Enabled support on Avant-G and Avant-X devices.

JESD204B IP v1.0.0

Software	Software Version	Summary of Changes
Lattice Radiant	2024.1	Initial release.

References

- JESD204B IP User Guide (FPGA-IPUG-02259)
- JESD204B Driver API Reference (FPGA-TN-02412)
- CertusPro-NX web page
- Avant-G web page
- Avant-X web page
- Certus-N2 web page
- JESD204B IP Core web page
- Lattice Radiant Software web page
- Lattice Propel Design Environment web page
- Lattice Solutions IP Cores web page
- Lattice Solutions Reference Designs web page
- Lattice Insights for Lattice Semiconductor training courses and learning plans

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For frequently asked questions, refer to the Lattice Answer Database at www.latticesemi.com/Support/AnswerDatabase

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