I3C Filter IP

IP Version: v1.7.0

Release Notes

FPGA-RN-02004-1.4

December 2025

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1. Introduction

This document contains the Release Notes for the I3C Filter IP and I3C Filter Driver. For specific details about the IP and driver, refer to the following:

- I3C Filter IP User Guide (FPGA-IPUG-02257)
- I3C Filter Driver API Reference (FPGA-TN-02390)
- I3C Filter IP Core web page

I3C Filter IP v1.7.0

Software	Software Version	Summary of Changes

Lattice Radiant
Lattice Propel Builder
	2025.2	• Added filtering disable option in the I2C filter mode.
		• Added bidirectional filtering support in the I2C filter mode.
		• Removed optional debug ports.
		• Removed the IP licensing requirement.
		• Driver update v25.2.0:
		  • Added new register offset at 0x1024 (Bus Status).
		  • Added new bits (filter_dis) to the filter configuration register at offset 0x1000.
		  • Added new driver API to enable or disable the filter function.

Corrections to Previous Release Notes

- All IP versions listed below are supported by the Lattice Propel Builder software.

I3C Filter IP v1.6.0

Software	Software Version	Summary of Changes
Lattice Radiant	2025.1	Added support for Mach-XO5 devices (LFMXO5-35, LFMXO5-35T, LFMXO5-65, and LFMXO5-65T).

I3C Filter IP v1.5.0

Software	Software Version	Summary of Changes
Lattice Radiant	2024.2	Initial driver release v24.2.0.

I3C Filter IP v1.4.0

Software	Software Version	Summary of Changes
Lattice Radiant	2024.2	Added optional debug ports.

I3C Filter IP v1.2.0

Software	Software Version	Summary of Changes
Lattice Radiant	2024.1	Initial release.

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References

- I3C Filter IP User Guide (FPGA-IPUG-02257)
- I3C Filter Driver API Reference (FPGA-TN-02390)
- MachXO5-NX web page
- I3C Filter IP Core web page
- Lattice Propel Design Environment web page
- Lattice Radiant Software web page
- Lattice Solutions IP Cores web page
- Lattice Solutions Reference Designs web page
- Lattice Insights web page for Lattice Semiconductor training courses and learning plans

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