

IP Configuration for Avant Family

LAV-AT-E70-3LFG1156I

Configuration	Clk Fmax (MHz)*	Registers	LUTs	EBRs
Configuration 1: Sequential architecture, 3 color planes, same color planes	201.45	97	18	1
Configuration 2: Parallel architecture, 3 color planes, same color planes	201.45	181	72	3
Configuration 3: Sequential architecture, 3 color planes, different color planes	201.45	97	18	1

***Note:**

Fmax is generated when the FPGA design only contains Gamma Corrector IP Core and the target Frequency is 200 MHz. These values may be reduced when user logic is added to the FPGA design.

IP Configuration for Nexus and older FPGA Families

LIFCL-40-9BG400I

Configuration	Clk Fmax (MHz)*	Registers	LUTs	EBRs	Programmable I/O
Configuration 1: Sequential architecture, 3 color planes, same color planes	200	130	27	2	35
Configuration 2: Parallel architecture, 3 color planes, same color planes	200	223	41	3	81
Configuration 3: Sequential architecture, 3 color planes, different color planes	200	132	31	3	35

LIFCL-17-7BG256I

Configuration	Clk Fmax (MHz)*	Registers	LUTs	EBRs	Programmable I/O
Configuration 1: Sequential architecture, 3 color planes, same color planes	200	130	27	2	35
Configuration 2: Parallel architecture, 3 color planes, same color planes	200	223	41	3	81
Configuration 3: Sequential architecture, 3 color planes, different color planes	200	132	31	3	35

Gamma Corrector Resource Utilization



LFD2NX-40-8BG256I

Configuration	Clk Fmax (MHz)*	Registers	LUTs	EBRs	Programmable I/O
Configuration 1: Sequential architecture, 3 color planes, same color planes	200	130	27	2	35
Configuration 2: Parallel architecture, 3 color planes, same color planes	200	223	41	3	81
Configuration 3: Sequential architecture, 3 color planes, different color planes	200	132	31	3	35

***Note:**

Fmax is generated when the FPGA design only contains Gamma Corrector IP Core and the target Frequency is 100 MHz. These values may be reduced when user logic is added to the FPGA design.

LFMXO5-25-9BBG400I

Configuration	Clk Fmax (MHz)*	Registers	LUTs	EBRs
Configuration 1: Sequential architecture, 3 color planes, same color planes	200	105	19	1
Configuration 2: Parallel architecture, 3 color planes, same color planes	200	205	73	3
Configuration 3: Sequential architecture, 3 color planes, different color planes	200	105	19	1

***Note:**

Fmax is generated when the FPGA design only contains Gamma Corrector IP Core and the target Frequency is 100 MHz. These values may be reduced when user logic is added to the FPGA design.

IP Configuration for ECP5¹

IP Express User-Configurable Configuration	SLICES	LUTs	Registers	sysMEM EBRs	f ^{MAX} (MHz)
Sequential architecture, 3 color planes, same color planes	57	27	113	3	270
Parallel architecture, 3 color planes, same color planes	133	36	265	9	270
Sequential architecture, 3 color planes, different color planes	52	24	104	3	270

1.Performance and utilization characteristics are generated using LFE5UM-45F-8BG554C with Lattice Diamond 3.4 software. When using this IP core in a different density, speed, or grade within the ECP5 family, performance and utilization may vary.

IP Configuration for ECP3¹

IP Express User-Configurable Configuration	SLICEs	LUTs	Registers	sysMEM EBRs	f ^{MAX} (MHz)
Sequential architecture, 3 color planes, same color planes	57	33	113	3	270
Parallel architecture, 3 color planes, same color planes	133	39	265	9	305
Sequential architecture, 3 color planes, different color planes	52	30	104	3	305

1. Performance and utilization characteristics are generated using LFE3-70EA-7F672C with Lattice Diamond 3.4 software. When using this IP core in a different density, speed, or grade within the LatticeECP3 family, performance and utilization may vary.

IP Configuration for LatticeXP2¹

IP Express User-Configurable Configuration	SLICEs	LUTs	Registers	sysMEM EBRs	f ^{MAX} (MHz)
Sequential architecture, 3 color planes, same color planes	57	27	113	3	310
Parallel architecture, 3 color planes, same color planes	133	36	265	9	310
Sequential architecture, 3 color planes, different color planes	52	24	104	3	310

1. Performance and utilization characteristics are generated using LFXP2-17E-7F484C with Lattice Diamond 3.4 software. When using this IP core in a different density, speed, or grade within the LatticeXP2 family, performance and utilization may vary.