

LPTM21L Breakout Board

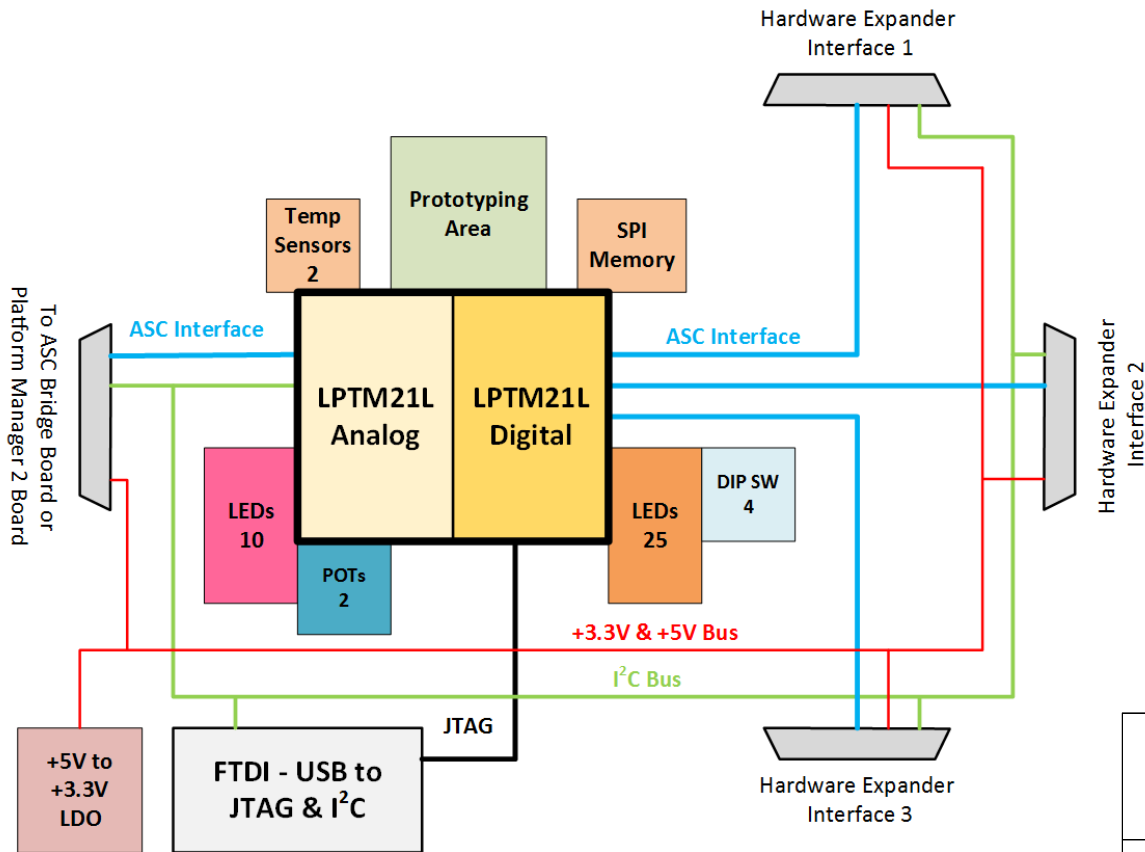
Rev B

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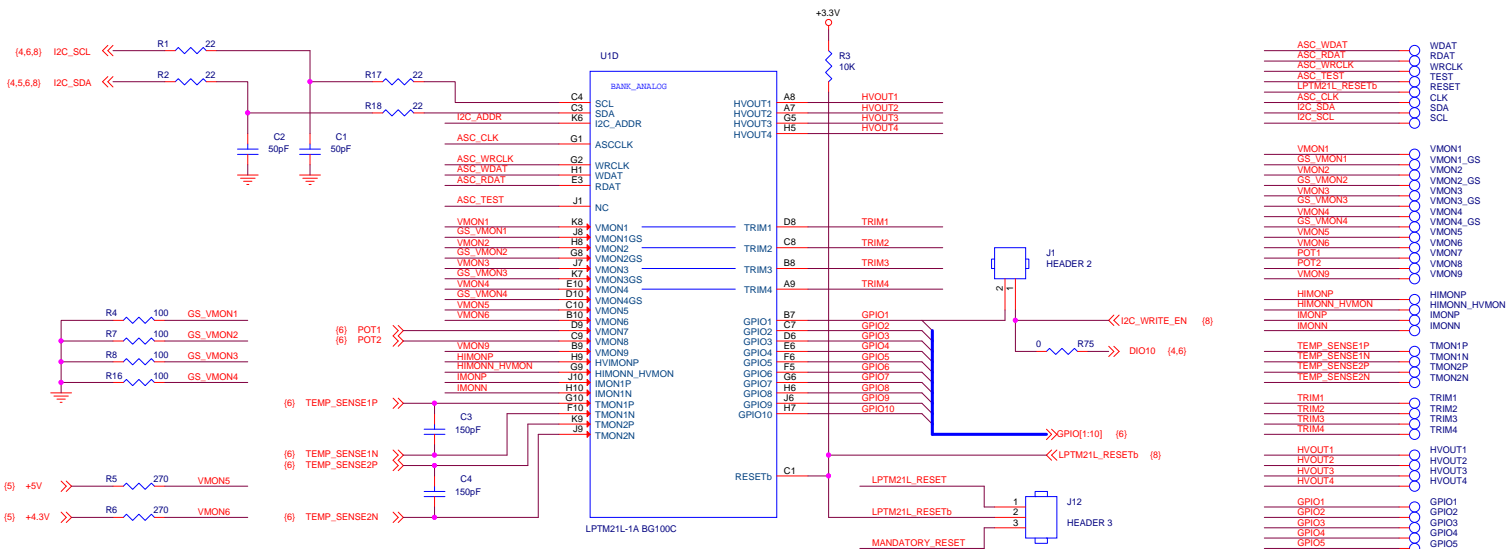
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Title			
Title Page			
Size	Project	Schematic Rev	
B	LPTM21L Breakout Board	B	
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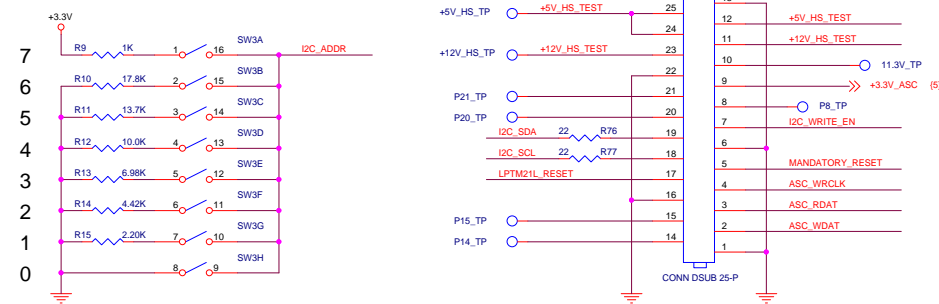
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Title			
Block Diagram			
Size	Project	Schematic Rev	B
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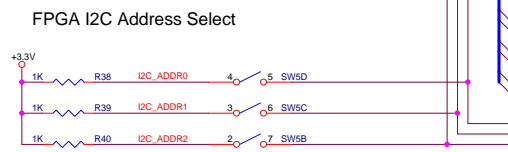
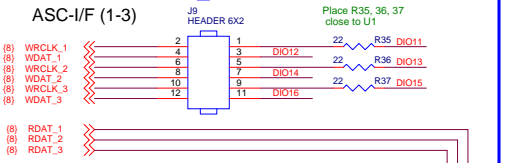
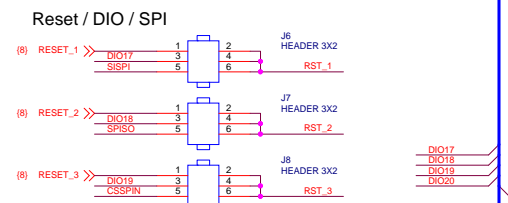
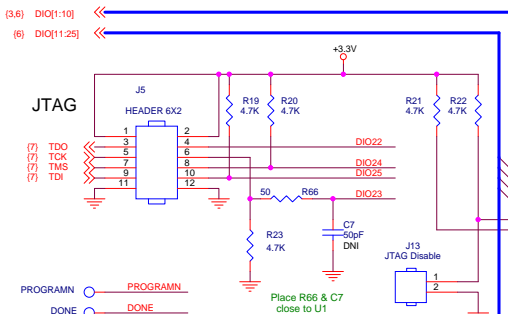
ASC Interface

LPTM21L I2C Address Select

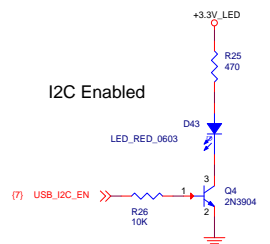
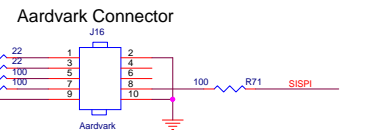
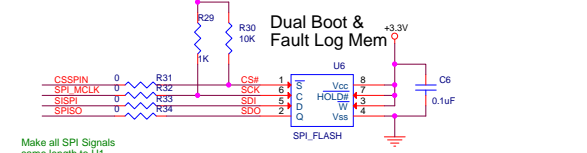
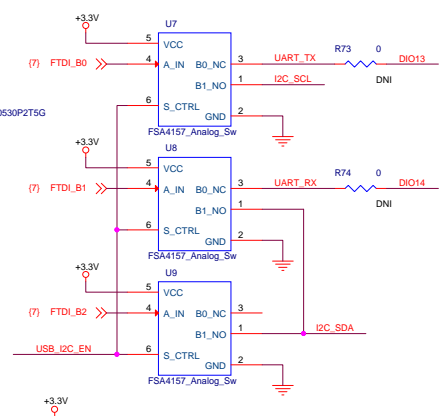
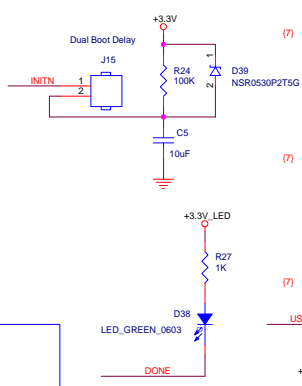
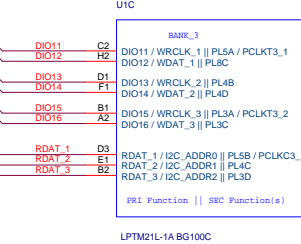
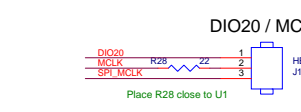
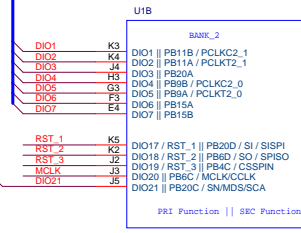
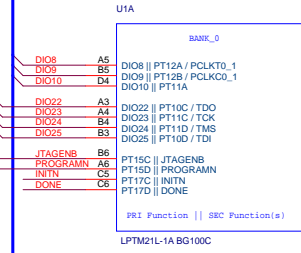


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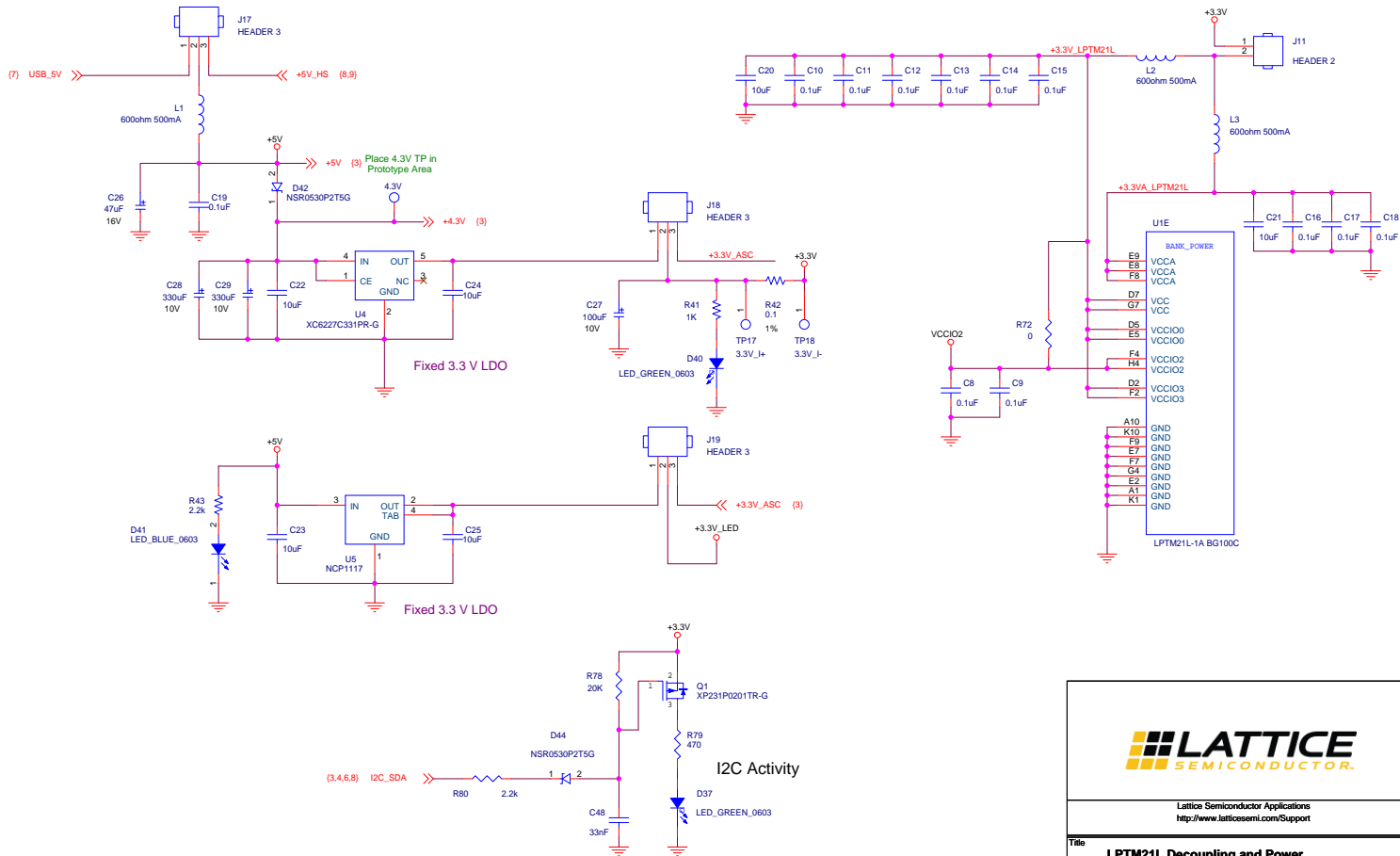
LPTM21L Analog Bank & ASC Interface			
Size	Project	Schematic Rev	
B	LPTM21L Breakout Board	B	B
Date:	Thursday 27-Feb-2020	Board Rev	B
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(See sheet 7 for other section of SW5)

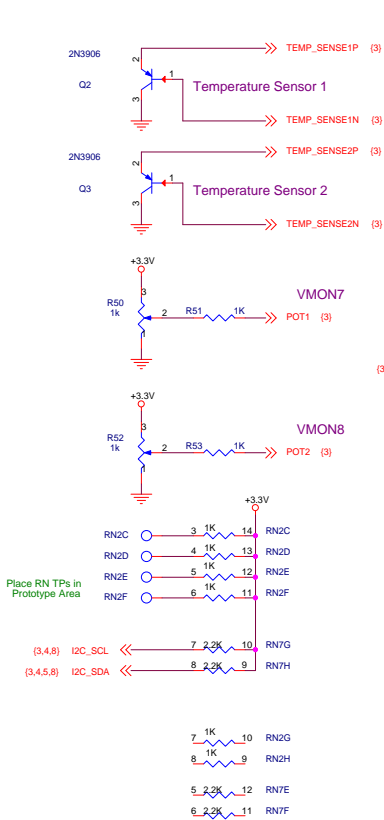


Title		LPTM21L Digital Banks & SPI Memory	
Size	Project	Sheet	B
B	LPTM21L Breakout Board	Board	B
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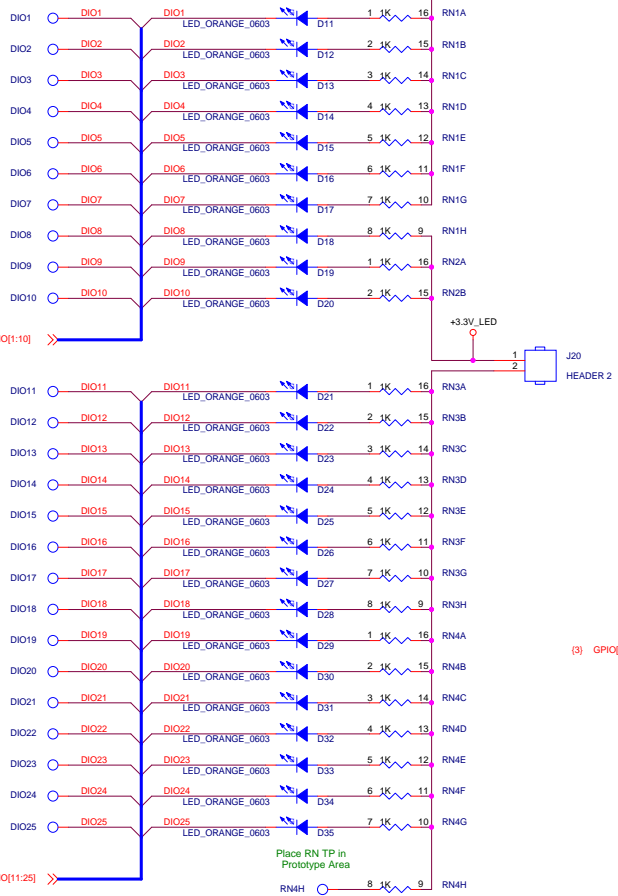


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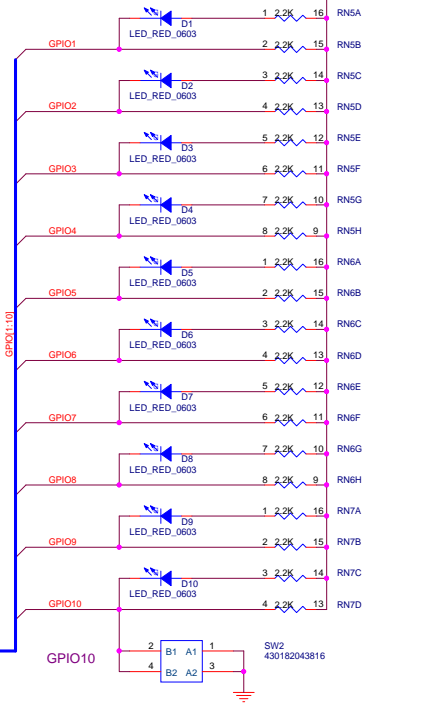
Title			LPTM21L Decoupling and Power		
Size	Project	Schematic Rev		B	
B	LPTM21L Breakout Board	Board Rev		B	
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DIO Test Points & LEDs

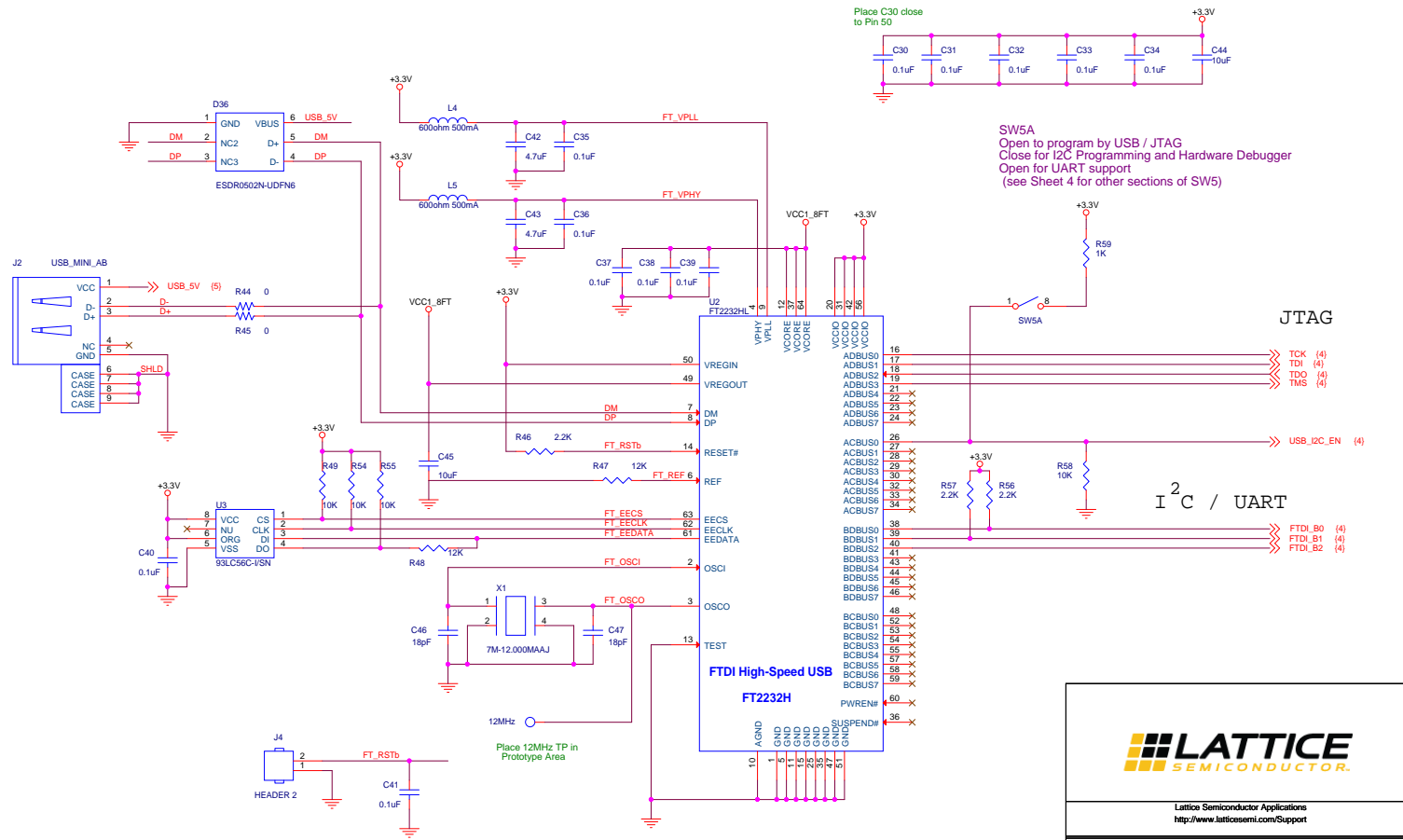


GPIO LEDs



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Title			
LEDs, POTs, TEMPs, and Switches			
Size	Project	Schematic Rev	
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SW5A
 Open to program by USB / JTAG
 Close for I2C Programming and Hardware Debugger
 Open for UART support
 (see Sheet 4 for other sections of SW5)

JTAG

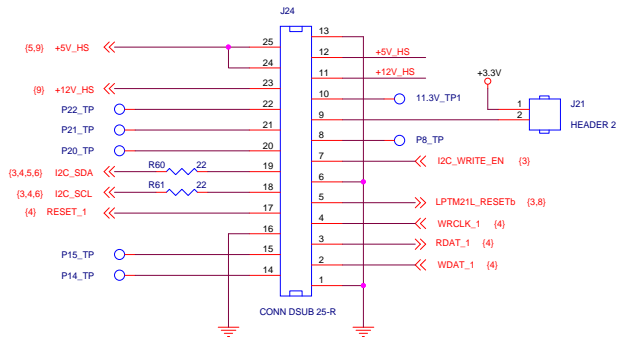
I²C / UART



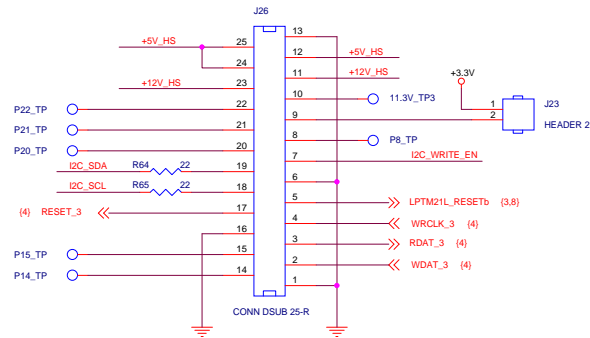
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Title		USB/FTDI Interface	
Size	Project	Schematic Rev	B
B	LPTM21L Breakout Board	Board Rev	B
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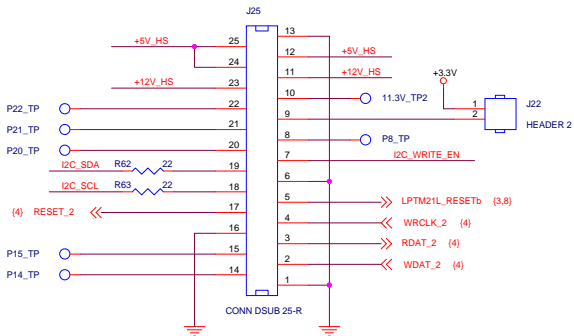
Hardware Expander Interface 1



Hardware Expander Interface 3



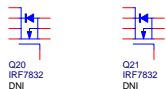
Hardware Expander Interface 2



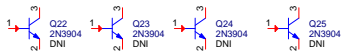
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Title			Hardware Expander Interface		
Size	Project			Schematic Rev	B
B	LPTM21L Breakout Board			Board Rev	B
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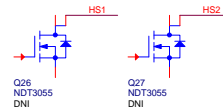
SOIC-8 Prototype Area



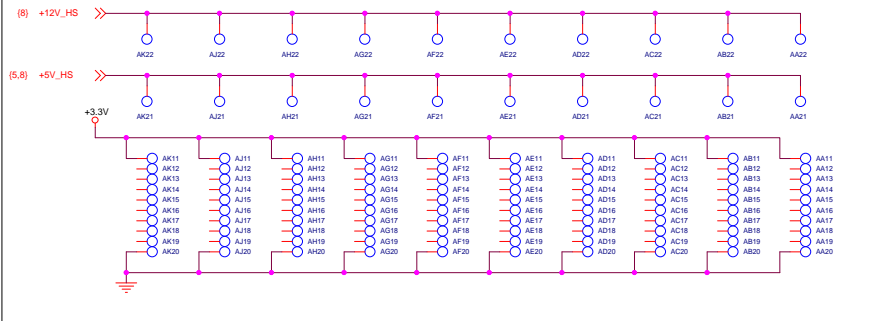
SOT-23 Prototype Area



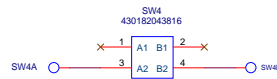
SOT-223 Prototype Area



Thru Hole Prototype Area



Prototype Push Button



SMD 0805 Cs, Ds, or Rs Prototype Area



SMD 2512 Resistor Package Prototype Area



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Title			Prototype		
Size	Project	Schematic Rev		B	
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