



AXI Register Slice IP

IP Version: v1.1.0

Release Notes

FPGA-RN-02049-1.0

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1. Introduction

This document contains the Release Notes for the AXI Register Slice IP. For specific details about the IP, refer to the following:

- [AXI Register Slice IP User Guide \(FPGA-IPUG-02235\)](#)

Note: These release notes document the changes from IP version 1.1.0 and Lattice Propel Builder software version 2024.2 and above.

AXI Register Slice IP v1.1.0

Software	Software Version	Summary of Changes
Lattice Propel Builder	2024.2	Added support for Certus-N2, LatticeECP3, and ECP5 devices.

AXI Register Slice IP v1.0.0 and Below

Software	Software Version	Summary of Changes
Lattice Propel Builder	2024.1 and below	Refer to the Release Notes section in <i>introduction.html</i> in the Propel Builder software installation folder or the IP server for details on changes made in previous versions of this IP.

References

- [AXI Register Slice IP User Guide \(FPGA-IPUG-02235\)](#)
- [LatticeECP3](#) web page
- [ECP5](#) web page
- [CertusPro-NX](#) web page
- [Certus-N2](#) web page
- [Avant-E](#) web page
- [Avant-G](#) web page
- [Avant-X](#) web page
- [AXI Register Slice IP Core](#) web page
- [Lattice Propel Builder](#) software
- [Lattice Radiant](#) FPGA design software
- [Lattice Diamond Software](#) web page
- [Lattice Insights](#) for Lattice Semiconductor training courses and learning plans

Technical Support Assistance

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For frequently asked questions, please refer to the Lattice Answer Database at www.latticesemi.com/Support/AnswerDatabase



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