



CSI-2/DSI D-PHY Rx IP

IP Version: v1.9.0

Release Notes

FPGA-RN-02040-1.0

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1. Introduction

This document contains the Release Notes for the CSI-2/DSI D-PHY Rx IP. For specific details about the IP, refer to the following:

- [CSI-2/DSI D-PHY Rx IP User Guide \(FPGA-IPUG-02081\)](#)

Note: These release notes document the changes from IP version 1.9.0 and Lattice Radiant software version 2024.2 and above.

CSI-2/DSI D-PHY Rx IP v1.9.0

Software	Software Version	Summary of Changes
Lattice Radiant	2024.2	<ul style="list-style-type: none"> • Removed the requirement to have free running byte clock (clk_byte_fr_i) active when accessing CSR. • Updated maximum data rate to be consistent with the Lattice device datasheet. • Enabled IMONDELAY logic for data rate 1,000 Mbps and above for Lattice Avant devices. • Added an example design. • Added support for Certus-N2 devices. • Added support for LFD2NX-9 and LFD2NX-28 devices. • Other minor IP enhancements and general bug fixes.

CSI-2/DSI D-PHY Rx IP v1.8.0 and Below

Software	Software Version	Summary of Changes
Lattice Radiant	2024.1 and below	Refer to the Release Notes section in <i>introduction.html</i> in the Radiant software installation folder or the IP server for details on changes made in previous versions of this IP.

References

- [CSI-2/DSI D-PHY Rx IP User Guide \(FPGA-IPUG-02081\)](#)
- [Certus-NX](#) web page
- [Certus-N2](#) web page
- [CertusPro-NX](#) web page
- [CrossLink-NX](#) web page
- [MachXO5-NX](#) web page
- [Avant-E](#) web page
- [Avant-G](#) web page
- [Avant-X](#) web page
- [CSI-2/DSI D-PHY Receiver IP Core](#) web page
- [Lattice Propel Builder](#) software
- [Lattice Radiant](#) FPGA design software
- [Lattice Insights](#) for Lattice Semiconductor training courses and learning plans

Technical Support Assistance

Submit a technical support case through www.latticesemi.com/techsupport.

For frequently asked questions, please refer to the Lattice Answer Database at www.latticesemi.com/Support/AnswerDatabase



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