



FIR Filter IP

IP Version: v2.2.0

Release Notes

FPGA-RN-02025-1.0

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1. Introduction

This document contains the Release Notes for the FIR Filter IP. For specific details about the IP, refer to the following:

- [FIR Filter IP User Guide \(FPGA-IPUG-02095\)](#)

Note: These release notes document the changes from IP version 2.2.0 and Lattice Radiant™ software version 2024.2 and above.

FIR Filter IP v2.2.0

Software	Software Version	Summary of Changes
Lattice Radiant	2024.2	<p>For the Lattice Avant™ Devices:</p> <ul style="list-style-type: none"> • Added multiplexing support of up to 64 independent channels, with up to 512 taps. • Increased the maximum number of taps for a single channel to 1,024 for a Direct-form Filter, and up to 2,048 for a Symmetric Filter. <p>For the Lattice Nexus™ 2 Devices:</p> <ul style="list-style-type: none"> • Added support for Certus-N2 devices.

FIR Filter IP v2.1.0 and Below

Software	Software Version	Summary of Changes
Lattice Radiant	2024.1 and below	Refer to the Release Notes section in <i>introduction.html</i> in the Radiant software installation folder or the IP server for details on changes made in previous versions of this IP.

References

- [FIR Filter IP User Guide \(FPGA-IPUG-02095\)](#)
- [Avant-E web page](#)
- [Avant-G web page](#)
- [Avant-X web page](#)
- [Certus-N2 web page](#)
- [Certus-NX web page](#)
- [CertusPro-NX web page](#)
- [CrossLink-NX web page](#)
- [MachXO5-NX web page](#)
- [FIR Filter IP Core web page](#)
- [Lattice Propel Design Environment web page](#)
- [Lattice Radiant Software web page](#)
- [Lattice Insights web page for Lattice Semiconductor training courses and learning plans](#)

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For frequently asked questions, please refer to the Lattice Answer Database at www.latticesemi.com/Support/AnswerDatabase.



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