



Pixel-to-Byte Converter IP

IP Version: v1.9.0

Release Notes

FPGA-RN-02020-1.0

December 2024

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1. Introduction

This document contains the Release Notes for the Pixel-to-Byte Converter IP. For specific details about the IP, refer to the following:

- [Pixel-to-Byte Converter IP User Guide \(FPGA-IPUG-02094\)](#)

Note: These release notes document the changes from IP version 1.9.0 and Lattice Radiant™ software version 2024.2 and above.

Pixel-to-Byte Converter IP v1.9.0

Software	Software Version	Summary of Changes
Lattice Radiant	2024.2	Added Word Count configurability when Manual Adjust is enabled.

Pixel-to-Byte Converter IP v1.8.0 and Below

Software	Software Version	Summary of Changes
Lattice Radiant	2024.1 and below	Refer to the Release Notes section in <i>introduction.html</i> in the Radiant software installation folder or the IP server for details on changes made in previous versions of this IP.

References

- [Pixel-to-Byte Converter IP User Guide \(FPGA-IPUG-02094\)](#)
- [Avant-E](#) web page
- [Avant-G](#) web page
- [Avant-X](#) web page
- [Certus-N2](#) web page
- [Certus-NX](#) web page
- [CertusPro-NX](#) web page
- [CrossLink-NX](#) web page
- [MachXO5-NX](#) web page
- [Pixel-to-Byte Converter IP Core](#) web page
- [Lattice Propel Design Environment](#) web page
- [Lattice Radiant Software](#) web page
- [Lattice Insights](#) web page for Lattice Semiconductor training courses and learning plans

Technical Support Assistance

Submit a technical support case through www.latticesemi.com/techsupport.

For frequently asked questions, please refer to the Lattice Answer Database at www.latticesemi.com/Support/AnswerDatabase.



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