



# Byte-to-Pixel Converter IP

IP Version: v1.9.0

## Release Notes

FPGA-RN-02019-1.0

February 2025

## Disclaimers

Lattice makes no warranty, representation, or guarantee regarding the accuracy of information contained in this document or the suitability of its products for any particular purpose. All information herein is provided AS IS, with all faults, and all associated risk is the responsibility entirely of the Buyer. The information provided herein is for informational purposes only and may contain technical inaccuracies or omissions, and may be otherwise rendered inaccurate for many reasons, and Lattice assumes no obligation to update or otherwise correct or revise this information. Products sold by Lattice have been subject to limited testing and it is the Buyer's responsibility to independently determine the suitability of any products and to test and verify the same. LATTICE PRODUCTS AND SERVICES ARE NOT DESIGNED, MANUFACTURED, OR TESTED FOR USE IN LIFE OR SAFETY CRITICAL SYSTEMS, HAZARDOUS ENVIRONMENTS, OR ANY OTHER ENVIRONMENTS REQUIRING FAIL-SAFE PERFORMANCE, INCLUDING ANY APPLICATION IN WHICH THE FAILURE OF THE PRODUCT OR SERVICE COULD LEAD TO DEATH, PERSONAL INJURY, SEVERE PROPERTY DAMAGE OR ENVIRONMENTAL HARM (COLLECTIVELY, "HIGH-RISK USES"). FURTHER, BUYER MUST TAKE PRUDENT STEPS TO PROTECT AGAINST PRODUCT AND SERVICE FAILURES, INCLUDING PROVIDING APPROPRIATE REDUNDANCIES, FAIL-SAFE FEATURES, AND/OR SHUT-DOWN MECHANISMS. LATTICE EXPRESSLY DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY OF FITNESS OF THE PRODUCTS OR SERVICES FOR HIGH-RISK USES. The information provided in this document is proprietary to Lattice Semiconductor, and Lattice reserves the right to make any changes to the information in this document or to any products at any time without notice.

## Inclusive Language

This document was created consistent with Lattice Semiconductor's inclusive language policy. In some cases, the language in underlying tools and other items may not yet have been updated. Please refer to Lattice's inclusive language [FAQ 6878](#) for a cross reference of terms. Note in some cases such as register names and state names it has been necessary to continue to utilize older terminology for compatibility.

# Contents

Contents .....	3
1. Introduction .....	4
Byte-to-Pixel Converter IP v1.9.0.....	4
Byte-to-Pixel Converter IP v1.7.0 and Below .....	4
References .....	5
Technical Support Assistance .....	6

# 1. Introduction

This document contains the Release Notes for the Byte-to-Pixel Converter IP. For specific details about the IP, refer to the following:

- [Byte-to-Pixel Converter IP User Guide \(FPGA-IPUG-02079\)](#)

**Note:** These release notes document the changes from IP version 1.9.0 and Lattice Radiant™ software version 2024.2 and above.

## Byte-to-Pixel Converter IP v1.9.0

Software	Software Version	Summary of Changes
Lattice Radiant	2024.2	<ul style="list-style-type: none"> <li>• Added support for the Lattice Certus™-N2 devices.</li> <li>• Added support for the AXI4-Lite interface protocol.</li> <li>• Added support for the AXI4-Stream protocol through Unified Video Streaming Tx interface.</li> <li>• Added the RGB remapping support to pd_red_o, pd_green_o, and pd_blue_o ports for DSI and CSI-2 RGB data types.</li> <li>• Added the <i>Enable Byte Swap per Pixel</i> attribute in the GUI.</li> <li>• Fixed the pd_o pixel output for RAW14 and YCbCr data types for MIPI compliance.</li> </ul>

## Byte-to-Pixel Converter IP v1.7.0 and Below

Software	Software Version	Summary of Changes
Lattice Radiant	2024.1 and below	Refer to the Release Notes section in <i>introduction.html</i> in the Radiant software installation folder or the IP server for details on changes made in previous versions of this IP.

## References

- [Byte-to-Pixel Converter IP User Guide \(FPGA-IPUG-02079\)](#)
- [Avant-E](#) web page
- [Avant-G](#) web page
- [Avant-X](#) web page
- [Certus-N2](#) web page
- [Certus-NX](#) web page
- [CertusPro-NX](#) web page
- [CrossLink-NX](#) web page
- [MachXO5-NX](#) web page
- [Byte-to-Pixel Converter IP Core](#) web page
- [Lattice Propel Design Environment](#) web page
- [Lattice Radiant Software](#) web page
- [Lattice Insights](#) web page for Lattice Semiconductor training courses and learning plans

## Technical Support Assistance

Submit a technical support case through [www.latticesemi.com/techsupport](http://www.latticesemi.com/techsupport).

For frequently asked questions, please refer to the Lattice Answer Database at [www.latticesemi.com/Support/AnswerDatabase](http://www.latticesemi.com/Support/AnswerDatabase).



[www.latticesemi.com](http://www.latticesemi.com)