



SPI Target IP

IP Version: v2.3.0

Release Notes

FPGA-RN-02014-1.0

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1. Introduction

This document contains the Release Notes for the SPI Target IP. For specific details about the IP, refer to the following:

- [SPI Target IP User Guide \(FPGA-IPUG-02070\)](#)

Note: These release notes document the changes from IP version 2.3.0 and Lattice Radiant software version 2024.2 and above.

SPI Target IP v2.3.0

Software	Software Version	Summary of Changes
Lattice Radiant	2024.2	<ul style="list-style-type: none"> • Added support for Certus-N2 device (LN2-CT-20). • Added support for Certus-NX devices (LFD2NX-9 and LFD2NX-28). • Added timing requirement from ss_i assertion to first edge of sclk_o in testbench and IP User Guide.

SPI Target IP v2.1.0 and Below

Software	Software Version	Summary of Changes
Lattice Radiant	2024.1 and below	Refer to the Release Notes section in <i>introduction.html</i> in the Radiant software installation folder or the IP server for details on changes made in previous versions of this IP.

References

- [SPI Target IP User Guide \(FPGA-IPUG-02070\)](#)
- [Avant-E](#) web page
- [Avant-G](#) web page
- [Avant-X](#) web page
- [Certus-N2](#) web page
- [Certus-NX](#) web page
- [CertusPro-NX](#) web page
- [CrossLink-NX](#) web page
- [MachXO5-NX](#) web page
- [SPI Target IP Core](#) web page
- [Lattice Propel Builder](#) software
- [Lattice Radiant](#) FPGA design software
- [Lattice Insights](#) for Lattice Semiconductor training courses and learning plans

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For frequently asked questions, please refer to the Lattice Answer Database at www.latticesemi.com/Support/AnswerDatabase



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