Dear Lattice Customer,

Lattice is providing this notification of our intent to revise the Lattice iCE40 UltraPlus™ Data Sheets. The revision pertains to the removal to support High frequency configuration using built-in NVCM.

See Table 1 for the summary of changes to the data sheets listed below.

- Lattice iCE40 UltraPlus Family Data sheet FPGA-DS-02008 Version 1.9 release in December 2020

The High Frequency condition will only be supported on the SPI Master. NVCM will not support the high frequency condition. This is indicated by note #3 in Data Sheet Table 4.23.

The high/fast frequency NVCM configuration is a supplemental function when the customer uses built-in NVCM to configure the device. This function is not active without explicit selection by the designer. Designs that do not use this function are not affected by this PCN. Designs that utilize this feature are referred to medium frequency NVCM configuration option.
**AFFECTED DEVICES**

This PCN affects all devices listed in Table 2.

NOTE: This PCN also affects any custom devices (i.e. factory programmed, special test, etc.) which are derived from any of the devices listed. All shipments of the indicated devices prior to this PCN are also affected. The OPNs remain unchanged for all affected devices.

<table>
<thead>
<tr>
<th>Product Family</th>
<th>Product Line</th>
<th>Package Type</th>
<th>Ordering Part Number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Lattice iCE40 UltraPlus</td>
<td>iCE40UP3K</td>
<td>30-ball WLCSP</td>
<td>iCE40UP3K-UWG30ITR</td>
</tr>
<tr>
<td></td>
<td></td>
<td>30-ball WLCSP</td>
<td>iCE40UP3K-UWG30ITR1K</td>
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<tr>
<td></td>
<td>iCE40UP5K</td>
<td>48-ball QFN</td>
<td>iCE40UP5K-SG48I</td>
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<td>iCE40UP5K-UWG30ITR</td>
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<tr>
<td></td>
<td></td>
<td>30-ball WLCSP</td>
<td>iCE40UP5K-UWG30ITR1K</td>
</tr>
</tbody>
</table>

*Table 2 - Affected Device List*

**DATA SHEET SPECIFICATIONS**

The revised Lattice UltraPlus data sheet (FPGA-DS-02008 Version 1.9, December 2020) reflects these changes.

**SOFTWARE SUPPORT**

Lattice Radiant Software 2.1 has been modified to remove Fast Oscillator Frequency for bit stream generation.

**TIMING**

These data sheet changes are effective immediately.

**RESPONSE**

Those customers using Fast Oscillator Frequency are advised to use Medium Oscillator Frequency for bit stream generation.

**CONTACT**

If you have any questions or require additional information, please contact pcn@latticesemi.com

Sincerely,

Lattice Semiconductor PCN Administration