Power Failure Protection for Solid State Drives

Reduce Hold Up Capacitor Size, Eliminate Boost Converter, Increase Reliability

Lattice power management solutions greatly simplify and increase the reliability of power failure protection circuitry in Solid State Drives (SSD). The power failure protection circuitry prevents data loss in the event of input power failure by using the on-board hold-up capacitor to provide supplemental power during that critical period when the data is fully saved into the Flash memory. These circuits traditionally use either a super capacitor or a tantalum capacitor bank along with a boost converter to store the energy.

The innovative Lattice Power Manager II device simplifies and reduces the cost of power failure protection circuitry by integrating the charging and power switch over circuit of the hold-up capacitor. In the case of tantalum hold-up capacitor applications, it eliminates the need for a voltage boost converter.

Power Manager II Family

The programmable Power Manager II family consists of six devices that can monitor and control up to 12 power supplies. The low power POWR607 is ideal for portable SSD applications, controlling up to six supplies and incorporating two high voltage MOSFET drivers. The POWR1014 and POWR1220 devices are well suited for enterprise or PCIe SSD applications. The POWR1220 can manage up to 12 voltages and drive 4 N-Channel MOSFETs for the most demanding SSD applications. It consists of a programmable threshold, precision differential input comparator block with an accuracy of 0.7%, a 48-macrocell CPLD, programmable hardware timers, a 10-bit analog to digital converter and a trim block for the trimming and margining of supplies.

Lattice mixed-signal product families are supported by development kits, reference designs and design software that enable fast, easy product development. For more information about Power Manager devices, visit http://www.latticesemi.com/products/powermanager.

Key Features and Benefits

- **Low Cost Solution** – Integrates:
  - Power failure protection
  - Hot swap controller
  - Sequencing controller
  - Reset generator
  - Voltage monitor
  - Hold up capacitor controller

- **Increase System Reliability**
  - Supervise up to 12 supplies
  - Respond to events in <65µs
  - Reduced component count
  - Controlled charging of hold up capacitor

- **32-Pin QFN, 48-Pin TQFP, 100-Pin TQFP Industrial Temperature**

- **In-System Re-programmability Reduces Risk**
  - On-chip configuration memory
  - JTAG programming interface

Reliable, Low Cost, Solid State Drive Power Back-up Management
Low Cost Alternative to Boost Converters

- Traditional Solutions Implement Boost Converters to Replace Super Capacitors with Less Expensive Tantalum Capacitors. The Higher Charge Voltages allow Increased Failure Protection Time.
- POWR607 Eliminates the Need for Additional Converters by Functioning as a Charge Pump for the Hold Up Capacitor

POWR607 – “Flexible, Integrated Solid State Drive Power Manager”

- 6 Programmable Threshold Monitors
  - 1% Step Size, 192 Steps
  - Typical Accuracy to 0.7%
  - Programmable Glitch Filter
  - Power-off Detection (75mV)
- 4 Timers
  - Individually Programmable from 32µs to 2 seconds
  - 8MHz Oscillator

Lattice’s Power Manager Family of Integrated/Programmable Voltage Monitors and FET Drivers Have a Wide Feature Set to Meet the Most Demanding Requirements of SSD Power Management.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>POWR605</th>
<th>POWR607</th>
<th>POWR1014</th>
<th>POWR1014A</th>
<th>POWR1220AT8</th>
</tr>
</thead>
<tbody>
<tr>
<td>Number of Programmable Voltage Monitors</td>
<td>6</td>
<td>6</td>
<td>10</td>
<td>10</td>
<td>12</td>
</tr>
<tr>
<td>Accuracy (Typ.)</td>
<td>0.5%</td>
<td>0.5%</td>
<td>0.3%</td>
<td>0.3%</td>
<td>0.2%</td>
</tr>
<tr>
<td>CPLD Macrocells</td>
<td>16</td>
<td>16</td>
<td>24</td>
<td>24</td>
<td>48</td>
</tr>
<tr>
<td>Programmable Digital I/Os</td>
<td>5</td>
<td>7</td>
<td>14</td>
<td>14</td>
<td>20</td>
</tr>
<tr>
<td>FET Drivers</td>
<td>–</td>
<td>2 (fixed)</td>
<td>2 (programmable)</td>
<td>2 (programmable)</td>
<td>2 (programmable)</td>
</tr>
<tr>
<td>ADC Resolution</td>
<td>–</td>
<td>–</td>
<td>10 (bits)</td>
<td>10 (bits)</td>
<td></td>
</tr>
<tr>
<td>I/O</td>
<td>–</td>
<td>–</td>
<td>Yes</td>
<td>Yes</td>
<td></td>
</tr>
<tr>
<td>Supply Current (Max.)</td>
<td>5</td>
<td>5</td>
<td>20</td>
<td>20</td>
<td>40</td>
</tr>
</tbody>
</table>

Applications Support
1-800-LATTICE (528-8423)
503-268-8001
technical_support@latticesemi.com

LATTICESEMI.COM