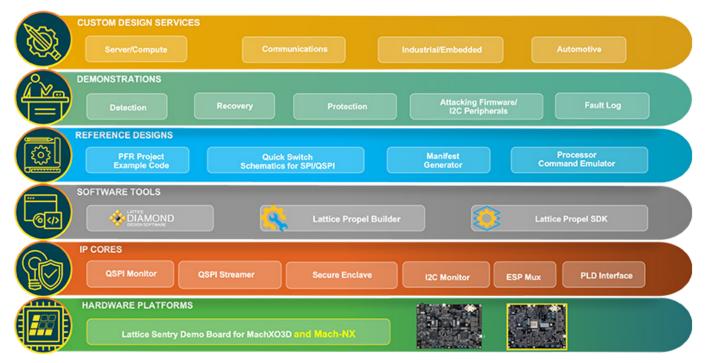


Lattice Sentry™ Solutions Stack

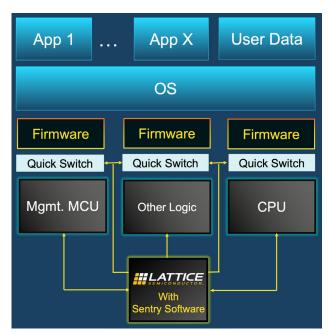
Dynamic PFR Solution for Comprehensive Coverage of NIST 800-193 Guidelines





Complete solution toolkit includes everything needed to create a custom platform firmware resiliency (PFR) Implementation

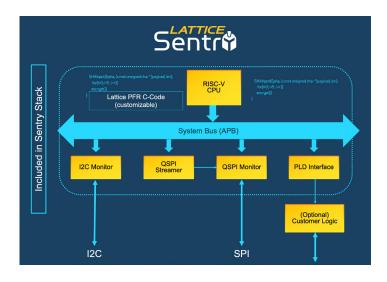
Solution allows secure protection of firmware before, during, and after system boot





Proven Lattice Sentry IP Cores

- QSPI Streamer
- QSPI Monitor
- I2C Monitor
- PLD Interface
- Embedded Security Block Mux
- RISC-V CPU



Easy To Use Lattice Design Tools





Plug & Play Lattice Sentry Reference Designs

- PFR Project Example Code
- QuickSwitch Schematics for SPI/QSPI

- Manifest Generator
- Processor Command Emulator

Instructive Lattice Sentry Demonstrations

- Protection
- Detection
- Recovery

- Attacking Firmware/I2C Peripherals
- Fault Log
- Implemented on Lattice Sentry Demo Board for MachXO3D

Lattice Sentry Custom Design Services

Have customized PFR needs for your design or market? The Lattice Sentry solution is fully customizable, and Lattice has a global Application Services staff who can perform custom IP development if needed. These customizations can enable a resilient PFR solution across a wide range of end-applications, including Communications, Industrial, Client Computing, Automotive and Data Center. Please contact your local Lattice sales agent to request more information.









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