

Data Sheet Revision History ispPAC-CLK5510/5520

| Date | Version | Page | Change Summary | Signed |
|-------------|----------------|-------------|--|---------------|
| June, 2004 | -02 | | Original Prelim Release to Web | |
| Aug 13/2004 | -04 | | Final Datasheet release to Web | |
| Aug 13/2004 | -04 | 5 | Max current added for 5510 | |
| Aug 13/2004 | -04 | 5 | 100 Ohm/5pF load notation added for LVDS | |
| Aug 13/2004 | -04 | 5,8,9,22,23 | ~20 Ohm notation replaces <40 Ohm setting for output termination | |
| Aug 13/2004 | -04 | 10 | Altered Max Period Jitter Spec | |
| Aug 13/2004 | -04 | 15,16 | NxV Max value extended from 32 to 64 | |
| Aug 13/2004 | -04 | 19 | Added differential SSTL receiver configuration graphic | |
| Aug 13/2004 | -04 | 24 | Altered 5510 Temperature Derating Curves for single ended operation | |
| Aug 13/2004 | -04 | 24 | Added Temperature Derating Curves for LVDS operation | |
| Aug 13/2004 | -04 | 27 | Added Skew Matching Accuracy section | |
| Aug 13/2004 | -04 | 29 | Added Reset and power up functional description | |
| Aug 13/2004 | -04 | 30 | Edited Eval Board part number | |
| Aug 13/2004 | -04 | 36,42 | Pin Names changed for 5510 | |
| Oct 1,2004 | -05 | 6 | Input Capacitance Spec broken out for Reference inputs versus control inputs | |
| Oct 1,2004 | -05 | 7 | Rise/Fall time values updated | |
| Oct 1,2004 | -05 | 8 | Output Test loads clarified, Figs 3,4,5 updated | |
| Oct 1,2004 | -05 | 9 | Input/Output Termination resistance table updated | |
| Oct 1,2004 | -05 | 15 | Additional Explanation of Lock Detector Behavior | |
| Oct 1,2004 | -05 | 16 | Added graph of PLL loop bandwidth versus feedback multiplication factor | |
| Oct 1,2004 | -05 | 18 | Output duty cycle tables restructured | |
| Oct 1,2004 | -05 | 19-20 | New Section on Output timing relationships | |
| Oct 1,2004 | -05 | 24 | Reference to Differential HSTL output capability clarified | |
| Feb 4,2005 | -06 | 9 | Output Impedance table modified | |
| Feb 4,2005 | -06 | 14-15 | Jitter Performance Vs VCO Freq graphs added | |
| Feb 22,2005 | -06.1 | 10 | Typical Phase Jitter & Static phase offset parameters added | |
| Mar 11,2005 | -06.2 | 36, 39 | Figure 30 Changed data register from 90 to 89, JTAG ID Code for ispPAC-CLK5510 corrected | |