

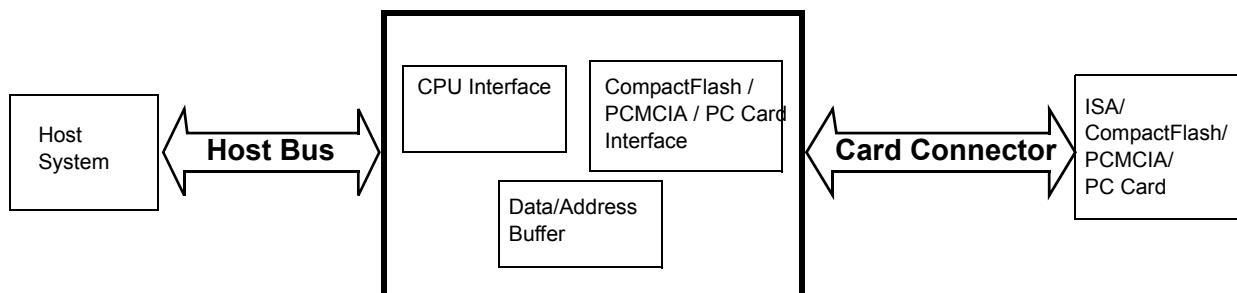


Product Summary

EP510 CompactFlash/PCMCIA Host Adapter

FEATURES

- Compliant with PC Card Standard 8.0, PCMCIA 2.1/JIEDA 4.2 and CompactFlash 1.4
- Allows host bus devices to access CompactFlash and PC Card/PCMCIA devices.
- Simple user interface optimized for on-chip bus connection.
- Dual clock design allows the user interface and the Card interface to operate at different clock domains.
- User interface supports 32-bit and 64-bit data.
- Option to integrate with other CPU bus slaves to support direct access by various CPU's including PowerPC, MPC860, ARM, SH2/3/4, MIPS, and ARC microprocessors.
- Option to support 82365SL-compatible and EXCA register set
- In PC Card/PCMCIA mode, supports attribute memory access, common memory access and IO access.
- In CompactFlash mode, supports attribute memory access, common memory access, IO access and IDE mode access.
- Converts 64/32-bit CPU access to multiple 8-bit or 16-bit accesses.
- Supports burst access by CPU. Burst access dispatched as multiple single accesses to the CompactFlash/PCMCIA interface.
- Direct mapping of host address space to card address space.
- CPU may operate at higher frequency while the card interface operates at lower frequency.
- Designed for ASIC and FPGA implementations.
- Fully static design with edge triggered flip-flops.





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DESCRIPTIONS

The host adapter supports all four access types as defined in the PC Card/PCMCIA/CompactFlash standards, including memory and IO access for ISA bus, common memory, attribute memory and IO access for PC Card/PCMCIA/The host adapter allows host CPU to access the PC Card/PCMCIA and CompactFlash cards. Different options of user interfaces are available. In the most basic form, the core contains a simple user interface which is optimized for on-chip connections. The user interface is modeled after generic microprocessor interface.

The core can also be integrated with other CPU bus slaves to interface directly with other embedded CPUs. The CPU supported are ARM (with AMBA AHB bus), PowerPC (with 60X bus or MPC860 bus), MIPS (with SysAD bus or EC interface), ARC and Hitachi SH2, SH3 and SH4 embedded processors.

The host adapter supports different types common memory access, attribute memory access and IO access for PC Card/PCMCIA/CompactFlash. It also supports True IDE mode in CompactFlash. Different chip select signal is provided for the CPU or user logic to select the address space being accessed.

The basic CPU interface can be 32 or 64-bit while the card interface can be either 8-bit or 16-bit. It supports burst and single data access by the CPU. When the CPU request reading of 32-bit of data from the card, the host adapter performs multiple 8-bit or 16-bit read operation to collect 32-bit data for the CPU. When the CPU writes 32-bit data to the card, the host adapter performs multiple 8-bit or 16-bit write to the card to write all the data. Burst access is not supported by the host adapter.

Direct address mapping is provided by the host adapter with address space selected by chip select input signals. Customized address translation scheme can be provided upon customer request.

The host adapter operates on two clock domains, the user interface clock domain and the card interface clock domain. All I/O signals on the user interface are timed by the rising edge of the user interface clock. All I/O signals on the card interface are timed by the rising edge of the card interface clock. Request/Acknowledge and double synchronization are used when transfer data between the clock domains. This design approach allows the user interface to run at the higher CPU bus clock frequency while the card interface runs at a lower clock frequency.

OPTIONAL FEATURES

The EP510 card controller supports the following optional features per customer specification

Optional features

Generic user interface or CPU specific interfaces including ARM, MIPS (SysAD and EC), PowerPC, ARC, SH
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EP510 CompactFlash/ PCMCIA Host Adapter

Optional features

82365SL-compatible and EXCA register set
External power switches devices
Dual card interface

Device Utilization

Family	Device	Utilization		Performance
		Slice	Percentage	
ispXPGA	LFX1200B	150	4%	63Mhz
EC	LFEC20	482	4%	80Mhz
XP	LFXP10	482	9%	88Mhz
ECP2	LFE2-12E	343	6%	150Mhz
SC	LFSC3GA15E	377	4%	200Mhz
XP2	LFXP2-17E	354	4%	150Mhz