



2.5G Ethernet IP

IP Version: v1.2.0

Release Notes

FPGA-RN-02083-1.2

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1. Introduction

This document contains the Release Notes for the 2.5G Ethernet IP. For specific details about the IP, refer to the following:

- [2.5G Ethernet IP User Guide \(FPGA-IPUG-02293\)](#)
- [2.5G, 10G, and 25G Ethernet Driver API Reference \(FPGA-TN-02375\)](#)

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Software	Software Version	Summary of Changes
Lattice Radiant™	2026.1	Improved LUT utilization.

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Software	Software Version	Summary of Changes
Lattice Radiant	2025.2	• Enabled Avant™-AT-G/X device support. • Enabled AXI4L register access support. • Improved timing optimization on CertusPro™-NX devices. • Removed MDIO interface. • Renamed IP name in the Radiant software from 2.5Gb Ethernet MAC + PHY to 2.5G Ethernet. • Removed MachXO5™-NX device support (LFMXO5-55T, LFMXO5-100T).

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Software	Software Version	Summary of Changes
Lattice Radiant	2025.1	Initial release.

References

- [2.5G Ethernet IP User Guide \(FPGA-IPUG-02293\)](#)
- [2.5G, 10G, and 25G Ethernet Driver API Reference \(FPGA-TN-02375\)](#)
- [2.5G Ethernet MAC + PHY IP](#) web page
- [CertusPro-NX](#) web page
- [MachXO5-NX](#) web page
- [Lattice Solutions IP Cores](#) web page
- [Lattice Solutions Reference Designs](#) web page
- [Lattice Solutions Boards](#) web page
- [Lattice Solutions Demonstrations](#) web page
- [Lattice Propel Builder](#) software web page
- [Lattice Radiant](#) FPGA design software web page
- [Lattice Insights](#) for Lattice Semiconductor training courses and learning plans

Technical Support Assistance

Submit a technical support case through www.latticesemi.com/techsupport.

For frequently asked questions, refer to the Lattice Answer Database at www.latticesemi.com/Support/AnswerDatabase



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