

January 23, 2012

Subject: PCN# 01A-12 Notification of Intent to Utilize an Alternate Qualified Mask Set and Foundry for the ispPAC-POWR Power Manager II Products

Dear Lattice Customers:

In accordance with our Product Change Notification (PCN) policy, Lattice is providing this 90-day notification of our intent to utilize an alternate qualified mask set and foundry for the ispPAC[®]-POWR Power Manager II products.

CHANGE DESCRIPTION

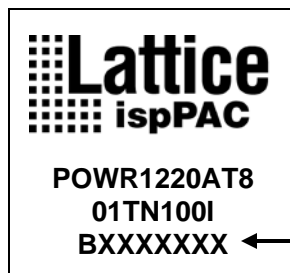
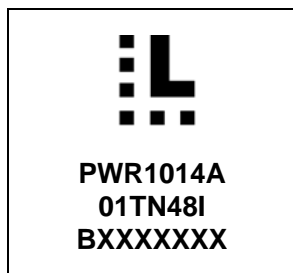
Currently, Lattice's ispPAC-POWR Power Manager II products are fabricated at UMC, Taiwan using Lattice's EE8A E²CMOS[®] process technology. In an effort to expand our fabrication capacity for these popular products, Lattice has ported them to our Seiko-Epson foundry in Sakata, Japan without making any design modifications. There are no changes to device form, fit or function.

AFFECTED DEVICES

The Ordering Part Numbers (OPNs) affected by this PCN are listed in Exhibit A. This PCN also affects any custom devices (i.e. factory programmed, special test, tape & reel, etc.), which are derived from any of the devices listed. This PCN does not affect the automotive versions of the ispPAC-POWR1014/A (OPNs: LA-ispPAC-POWR1014/A).

DEVICE IDENTIFICATION

Devices with this new alternate qualified mask set can be identified by the first alpha character of the Inspection Lot Number "B", which is marked on the topside of the devices. Inspection lot numbers are also marked on the label ("Mark Code" field) on the outside of the inventory box as well as on the anti-static, moisture barrier bag within. See device topside marking examples below.



← Inspection Lot Number

DATA SHEET SPECIFICATIONS

This PCN has no impact on any data sheet specifications.

QUALIFICATION DATA

Reliability testing for the qualification of the ispPAC-POWR Power Manager II devices is complete. Qualification data pertinent to this PCN is identified by the “Change Summary” column of revision entry “C” (issued November 2011) on the last page of the document available [here](#).

CHARACTERIZATION DATA

A device characterization report is available [here](#).

CONVERSION TIMING

Conversion timing for this PCN is 90 days from the date of this Notice. Should samples be required to complete evaluation of this PCN, such sample requests must be received **no later than February 22, 2012** (30 days after the date of this Notice).

CONVERSION TIMING – SUMMARY

- **Sample Request Cut-off Date: February 22, 2012**
- **PCN Expiration Date: April 23, 2012**

RESPONSE

In accordance with JESD46-C, this change is deemed accepted by the customer if no acknowledgement is received within 30 days from this Notice.

Lattice PCNs are available on the [Lattice website](#). Please sign up to receive e-mail PCN alerts by registering [here](#). If you already have Lattice web account and wish to receive PCN alerts, you can do so by logging in your account and make edits to your subscription options.

CONTACT

If you have any questions or require additional information, please contact pcn@latticesemi.com.

Sincerely,

Lattice Semiconductor PCN Administration

EXHIBIT “A” – Affected Device List

Product Line	Ordering Part Number	Package
ispPAC-POWR605	ispPAC-POWR605-01SN24I	Pb-Free 24-QFNS
ispPAC-POWR607	ispPAC-POWR607-01S32I	32-QFNS
	ispPAC-POWR607-01SN32I	Pb-Free 32-QFNS
ispPAC-POWR1014	ispPAC-POWR1014-01T48I	48-TQFP
	ispPAC-POWR1014-02T48I	
	ispPAC-POWR1014-01TN48I	Pb-Free
	ispPAC-POWR1014-02TN48I	48-TQFP
ispPAC-POWR1014A	ispPAC-POWR1014A-01T48I	48-TQFP
	ispPAC-POWR1014A-02T48I	
	ispPAC-POWR1014A-01TN48I	Pb-Free
	ispPAC-POWR1014A-02TN48I	48-TQFP
ispPAC-POWR1220AT8	ispPAC-POWR1220AT8-01T100I	100-TQFP
	ispPAC-POWR1220AT8-02T100I	
	ispPAC-POWR1220AT8-01TN100I	Pb-Free
	ispPAC-POWR1220AT8-02TN100I	100-TQFP
ispPAC-POWR6AT6	ispPAC-POWR6AT6-01S32I	32-QFNS
	ispPAC-POWR6AT6-01SN32I	Pb-Free 32-QFNS

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