MachXO3: BRIDGING & I/O PROBLEMS SOLVED

VOLTAGE MISMATCHES

PROBLEM
A device only accepts one I/O voltage level.

SOLUTION
MachXO3 enables a device to accept different I/O voltages.

SLOW OR LIMITED PERFORMANCE

PROBLEM
Processor fails to multi-task or performance drop due to processing too much information.

SOLUTION
MachXO3 offers better timing critical functionality to increase performance.

PROCESSOR LACKS THE REQUIRED FUNCTIONALITY

PROBLEM
Legacy processor lacks latest functionality.

SOLUTION
MachXO3 rapidly takes on new functionality allowing processor design reuse.

PROCESSOR LACKS NECESSARY INTERFACES

PROBLEM
Processor has either incorrect or insufficient interfaces.

SOLUTION
MachXO3 provides multiple new and additional interfaces.

INFLExIBILITY

PROBLEM
Major system design changes are required to implement new features.

SOLUTION
MachXO3 allows for rapid design changes.

HIGH POWER CONSUMPTION

PROBLEM
Always On central processors consume too much power.

SOLUTION
MachXO3 decreases power by staying awake in low power mode and only waking the processor when needed.

NOT ENOUGH I/O

PROBLEM
Processor has sufficient I/O to connect required interface devices, chips and sensors.

SOLUTION
MachXO3 expands available I/O without changing main processor platform design.

COST

PROBLEM
Large processors and large FPGAs are expensive.

SOLUTION
MachXO3 provides interface bridging IP that allows designers to leverage innovative technologies without the product design.

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NEED MORE INFORMATION?

www.latticesemi.com/MachXO3

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