INTRODUCING LATTICE’S CROSSLINK™ SOLUTION
The World’s First FPGA with Hardened MIPI D-PHY for Image Sensor and Display Interface Bridging

WHY CROSSLINK?
Solves the problem of interface mismatches

BRIDGING THE VIDEO CONNECTIVITY GAP
One Bridge. Endless Possibilities.

LATTERM VS. THE COMPETITION
- 50% Higher I/O performance 12 Gbps
- 2X – 3X Smaller footprint 6 mm² option
- 20% – 50% Lower power in active mode

Your Link to Advanced Connectivity: www.latticesemi.com/CrossLink